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MULTIPROCESSOR EXECUTION OF LOGIC PROGRAMS

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Multiprocessor Execution Of Logic Programs

Peter Kacsuk, Michael Wise



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Multiprocessor Execution of Logic Programs Gopal Gupta, 2012-12-06 Multiprocessor Execution of Logic Programs addresses the problem of efficient implementation of logic programming languages specifically Prolog on multiprocessor architectures. The approaches and implementations developed attempt to take full advantage of sequential implementation technology developed for Prolog such as the WAM while exploiting all forms of control parallelism present in logic programs namely or parallelism independent and parallelism and dependent and parallelism. Coverage includes a thorough survey of parallel implementation techniques and parallel systems developed for Prolog. Multiprocessor Execution of Logic Programs is recommended for people implementing parallel logic programming systems parallel symbolic systems parallel AI systems and parallel theorem proving systems. It will also be useful to people who wish to learn about the implementation of parallel logic programming systems.

Multiprocessor Execution of Logic Programs Gopal Gupta, 1994-10-31 Addresses the efficient implementation of logic programming languages specifically PROLOG on multiprocessor architectures. Develops approaches and implementations aimed at taking full advantage of sequential implementation technology developed for PROLOG while exploiting all forms of control parallelism present in logic programs. Covers the analysis of parallel execution models combining independent and parallelism and or parallelism and extensions and variations of the and or model. Annotation copyright by Book News Inc Portland OR.

Parallel Execution of Logic Programs John S. Conery, 2012-12-06 This book is an updated version of my Ph D dissertation The AND OR Process Model for Parallel Interpretation of Logic Programs. The three years since that paper was finished or so I thought then have seen quite a bit of work in the area of parallel execution models and programming languages for logic programs. A quick glance at the bibliography here shows roughly 50 papers on these topics 40 of which were published after 1983. The main difference between the book and the dissertation is the updated survey of related work. One of the appendices in the dissertation was an overview of a Prolog implementation of an interpreter based on the AND OR Process Model a simulator I used to get some preliminary measurements of parallelism in logic programs. In the last three years I have been involved with three other implementations. One was written in C and is now being installed on a small multiprocessor at the University of Oregon. Most of the programming of this interpreter was done by Nitin More under my direction for his M S project. The other two one written in Multilisp and the other in Modula 2 are more limited intended to test ideas about implementing specific aspects of the model. Instead of an appendix describing one interpreter this book has more detail about implementation included in Chapters 5 through 7 based on a combination of ideas from the four interpreters.

AND-parallel Execution of Logic Programs on a Shared Memory Multiprocessor: a Summary of Results University of Texas at Austin. Artificial Intelligence Laboratory, University of Texas at Austin. Dept. of Computer Sciences, Yow-Jian Lin, Vipin Kumar, 1988.

Parallel Execution of Logic Programs Anthony Beaumont, Gopal Gupta, 1991-12-11 Logic programming refers to execution of programs written in Horn logic. Among the advantages of this

style of programming are its simple declarative and procedural semantics high expressive power and inherent nondeterminism The papers included in this volume were presented at the Workshop on Parallel Logic Programming held in Paris on June 24 1991 as part of the 8th International Conference on Logic Programming The papers represent the state of the art in parallel logic programming and report the current research in this area including many new results The three essential issues in parallel execution of logic programs which the papers address are Which forms of parallelism or parallelism and parallelism stream parallelism data parallelism etc will be exploited Will parallelism be explicitly programmed by programmers or will it be exploited implicitly without their help Which target parallel architecture will the logic programs run on

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Parallel Combinatorial Optimization El-Ghazali Talbi, 2006-12-13 This text provides an excellent balance of theory and application that enables you to deploy powerful algorithms frameworks and methodologies to solve complex optimization problems in a diverse range of industries Each chapter is written by leading experts in the fields of parallel and distributed optimization Collectively the contributions serve as a complete reference to the field of combinatorial optimization including details and findings of recent and ongoing investigations

Parallel Computing on Distributed Memory Multiprocessors Füsün Özgüner, Fikret Ercal, 2012-12-06 Advances in microelectronic technology have made massively parallel computing a reality and triggered an outburst of research activity in parallel processing architectures and algorithms Distributed memory multiprocessors parallel computers that consist of microprocessors connected in a regular topology are increasingly being used to solve large problems in many application areas In order to use these computers for a specific application existing algorithms need to be restructured for the architecture and new algorithms developed The performance of a computation on a distributed memory multiprocessor is affected by the node and communication architecture the interconnection network topology the I/O subsystem and the parallel algorithm and communication protocols Each of these parameters is a complex problem and solutions require an understanding of the interactions among them This book is based on the papers presented at the NATO Advanced Study Institute held at Bilkent University Turkey in July 1991 The book is organized in five parts Parallel computing structures and

communication Parallel numerical algorithms Parallel programming Fault tolerance and Applications and algorithms

Logic Programming David S. Warren,1993 The Tenth International Conference on Logic Programming sponsored by the Association for Logic Programming is a major forum for presentations of research applications and implementations in this important area of computer science Logic programming is one of the most promising steps toward declarative programming and forms the theoretical basis of the programming language Prolog and its various extensions Logic programming is also fundamental to work in artificial intelligence where it has been used for nonmonotonic and commonsense reasoning expert systems implementation deductive databases and applications such as computer aided manufacturing David S Warren is Professor of Computer Science at the State University of New York Stony Brook Topics covered Theory and Foundations Programming Methodologies and Tools Meta and Higher order Programming Parallelism Concurrency Deductive Databases Implementations and Architectures Applications Artificial Intelligence Constraints Partial Deduction Bottom Up Evaluation Compilation Techniques **Transputer Applications and Systems '93** Reinhard Grebe,1993 Proceedings Parallel Computing *Parallel Computing Technologies* Victor Malyshev,1995-08-16 Proceedings Parallel Computing

Proceedings 20th International Conference Parallel Processing 1991 Tse-yun Feng,1991-08-06 **Pool Machine** Khayri A. M. Ali,1985 Abstract We propose a new multiprocessor architecture for fast OR parallel execution of logic programs The architecture supports a mechanism which is based on extending the depth first sequential processing to include parallel processing The architecture has n processing elements PEs m memory modules MMs where $m \leq n$ and a special interconnection network Each PE sequentially processes a search tree by a depth first search Each PE can be connected to a number of MMs It simultaneously constructs identical copies of its current tree in each of the connected MMs Each PE splits its current tree for OR parallel processing only when other PEs are idle **Knowledge Based Computer Systems** S. Ramani,R. Chandrasekar,K.S.R. Anjaneyulu,1990-07-24 This volume presents selected papers from KBCS 89 which is the second in a series of annual conferences hosted by the Knowledge Based Computer Systems Project funded by the Government of India with United Nations assistance The papers are grouped into sections including AI applications computer architecture and parallel processing expert systems intelligent tutoring systems knowledge representation logic programming natural language understanding pattern recognition reasoning search activities at the KBCS Nodal Centres

Encyclopedia of Computer Science and Technology Allen Kent,James G. Williams,2000-04-28 Combining Artificial Neural Networks to Symbolic and Algebraic computation **ACM Transactions on Programming Languages and Systems** Association for Computing Machinery,2001 **Parallel Execution of Logic Programs on Shared Memory Multiprocessors** Gopal Gupta,1991 *Parallel execution of logic programs on distributed memory multiprocessors* Youjun Luo,1995 *Logic Programming* Robert Kowalski,Kenneth A. Bowen,1988 These two volumes collect papers presented at the first joint meeting of the two principal logic programming conferences held in August of 1988 The more than fifty

contributions cover all aspects of the field including applications particularly those that exploit the unique character of logic programming the role of logic programming in artificial intelligence deductive databases relations to other computational paradigms language issues methodology implementations on sequential and parallel architectures and theory Logic Programming is included in the Logic Programming series Research Reports and Notes edited by Ehud Shapiro

Implementations of Distributed Prolog Peter Kacsuk, Michael Wise, 1992-08-11 Leading international contributors present papers pertaining to current approaches in the design and implementation of distributed logic languages that are a generation beyond standard Prolog all of them introducing ideas of distributed and parallel programming in an attempt to bring high performance features to logic programming applications Details all aspects of underlying research at a number of global centers including language implementation models and techniques hardware architecture performance results applications and more

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