

Materials Fundamentals of Gate Dielectrics

Edited by

Alexander A. Demkov and
Alexandra Navrotsky



Materials Fundamentals Of Gate Dielectrics

C. R. Abernathy

Materials Fundamentals Of Gate Dielectrics:

Materials Fundamentals of Gate Dielectrics Alexander A. Demkov,Alexandra Navrotsky,2006-05-24 This book presents materials fundamentals of novel gate dielectrics that are being introduced into semiconductor manufacturing to ensure the continuous scaling of the CMOS devices This is a very fast evolving field of research so we choose to focus on the basic understanding of the structure thermodynamics and electronic properties of these materials that determine their performance in device applications Most of these materials are transition metal oxides Ironically the d orbitals responsible for the high dielectric constant cause severe integration difficulties thus intrinsically limiting high k dielectrics Though new in the electronics industry many of these materials are well known in the field of ceramics and we describe this unique connection The complexity of the structure property relations in TM oxides makes the use of the state of the art first principles calculations necessary Several chapters give a detailed description of the modern theory of polarization and heterojunction band discontinuity within the framework of the density functional theory Experimental methods include oxide melt solution calorimetry and differential scanning calorimetry Raman scattering and other optical characterization techniques transmission electron microscopy and x ray photoelectron spectroscopy Many of the problems encountered in the world of CMOS are also relevant for other semiconductors such as GaAs A comprehensive review of recent developments in this field is thus also given The book should be of interest to those actively engaged in the gate dielectric research and to graduate students in Materials Science Materials Physics Materials Chemistry and Electrical Engineering

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students in Materials Science Materials Physics Materials Chemistry and Electrical Engineering

High-k Gate Dielectric Materials Niladri Pratap Maity,Reshmi Maity,Srimanta Baishya,2020-12-18 This volume explores and addresses the challenges of high k gate dielectric materials one of the major concerns in the evolving semiconductor industry and the International Technology Roadmap for Semiconductors ITRS The application of high k gate dielectric materials is a promising strategy that allows further miniaturization of microelectronic components This book presents a broad review of SiO₂ materials including a brief historical note of Moore's law followed by reliability issues of the SiO₂ based MOS transistor It goes on to discuss the transition of gate dielectrics with an EOT 1 nm and a selection of high k materials A review of the various deposition techniques of different high k films is also discussed High k dielectrics theories quantum tunneling effects and interface engineering theory and applications of different novel MOSFET structures like tunneling FET are also covered in this book The volume also looks at the important issues in the future of CMOS technology and presents an analysis of interface charge densities with the high k material tantalum pentoxide The issue of CMOS VLSI technology with the high k gate dielectric materials is covered as is the advanced MOSFET structure with its working structure and modeling This timely volume will prove to be a valuable resource on both the fundamentals and the successful integration of high k dielectric materials in future IC technology

High-k Gate Dielectric Materials Niladri Pratap Maity,Reshmi Maity,Srimanta Baishya,2020-12-17 This volume explores and addresses the challenges of high k gate dielectric materials one of the major concerns in the evolving semiconductor industry and the International Technology Roadmap for Semiconductors ITRS The application of high k gate dielectric materials is a promising strategy that allows further miniaturization of microelectronic components This book presents a broad review of SiO₂ materials including a brief historical note of Moore's law followed by reliability issues of the SiO₂ based MOS transistor It goes on to discuss the transition of gate dielectrics with an EOT 1 nm and a selection of high k materials A review of the various deposition techniques of different high k films is also discussed High k dielectrics theories quantum tunneling effects and interface engineering theory and applications of different novel MOSFET structures like tunneling FET are also covered in this book The volume also looks at the important issues in the future of CMOS technology and presents an analysis of interface charge densities with the high k material tantalum pentoxide The issue of CMOS VLSI technology with the high k gate dielectric materials is covered as is the advanced MOSFET structure with its working structure and modeling This timely volume will prove to be a valuable resource on both the fundamentals and the successful integration of high k dielectric materials in future IC technology

High Temperature Materials and Mechanisms Yoseph Bar-Cohen,2014-03-03 The use of high temperature materials in current and future applications including silicone materials for handling hot foods and metal alloys for developing high speed aircraft and spacecraft systems has generated a growing interest in high temperature technologies High Temperature Materials and Mechanisms explores a broad range of issues related to high temperature materials and mechanisms that operate in harsh

conditions While some applications involve the use of materials at high temperatures others require materials processed at high temperatures for use at room temperature High temperature materials must also be resistant to related causes of damage such as oxidation and corrosion which are accelerated with increased temperatures This book examines high temperature materials and mechanisms from many angles It covers the topics of processes materials characterization methods and the nondestructive evaluation and health monitoring of high temperature materials and structures It describes the application of high temperature materials to actuators and sensors sensor design challenges as well as various high temperature materials and mechanisms applications and challenges Utilizing the knowledge of experts in the field the book considers the multidisciplinary nature of high temperature materials and mechanisms and covers technology related to several areas including energy space aerospace electronics and metallurgy Supplies extensive references at the end of each chapter to enhance further study Addresses related science and engineering disciplines Includes information on drills actuators sensors and more A comprehensive resource of information consolidated in one book this text greatly benefits students in materials science aerospace and mechanical engineering and physics It is also an ideal resource for professionals in the industry

High Permittivity Gate Dielectric Materials Samares Kar,2013-06-25 The book comprehensively covers all the current and the emerging areas of the physics and the technology of high permittivity gate dielectric materials including topics such as MOSFET basics and characteristics hafnium based gate dielectric materials Hf based gate dielectric processing metal gate electrodes flat band and threshold voltage tuning channel mobility high k gate stack degradation and reliability lanthanide based high k gate stack materials ternary hafnia and lanthania based high k gate stack films crystalline high k oxides high mobility substrates and parameter extraction Each chapter begins with the basics necessary for understanding the topic followed by a comprehensive review of the literature and ultimately graduating to the current status of the technology and our scientific understanding and the future prospects

Dielectric Polymer Nanocomposites J. Keith Nelson,2009-12-17 Dielectric Polymer Nanocomposites provides the first in depth discussion of nano dielectrics an emerging and fast moving topic in electrical insulation The text begins with an overview of the background principles and promise of nanodielectrics followed by a discussion of the processing of nanocomposites and then proceeds with special considerations of clay based processes mechanical thermal and electric properties and surface properties as well as erosion resistance Carbon nanotubes are discussed as a means of creation of non linear conductivity the text concludes with a industrial applications perspective

In Situ Characterization of Thin Film Growth Gertjan Koster,Guus Rijnders,2011-10-05 Advanced techniques for characterizing thin film growth in situ help to develop improved understanding and faster diagnosis of issues with the process In situ characterization of thin film growth reviews current and developing techniques for characterizing the growth of thin films covering an important gap in research Part one covers electron diffraction techniques for in situ study of thin film growth including chapters on topics such as reflection high energy

electron diffraction RHEED and inelastic scattering techniques Part two focuses on photoemission techniques with chapters covering ultraviolet photoemission spectroscopy UPS X ray photoelectron spectroscopy XPS and in situ spectroscopic ellipsometry for characterization of thin film growth Finally part three discusses alternative in situ characterization techniques Chapters focus on topics such as ion beam surface characterization real time in situ surface monitoring of thin film growth deposition vapour monitoring and the use of surface x ray diffraction for studying epitaxial film growth With its distinguished editors and international team of contributors In situ characterization of thin film growth is a standard reference for materials scientists and engineers in the electronics and photonics industries as well as all those with an academic research interest in this area Chapters review electron diffraction techniques including the methodology for observations and measurements Discusses the principles and applications of photoemission techniques Examines alternative in situ characterisation techniques

Implantable Neural Prostheses 2 David Zhou,Elias Greenbaum,2010-07-10 Signi cant progress has been made in the development of neural prostheses for restoration of human functions and improvement of the quality of life Biomedical engineers and neuroscientists around the world are working to improve the design and performance of existing devices and to develop novel devices for arti cial vision arti cial limbs and brain machine interfaces This book Implantable Neural Prostheses 2 Techniques and Engineering Approaches is part two of a two volume sequence that describes state of the art advances in techniques associated with implantable neural prosthetic devices The techniques covered include biocompatibility and biostability hermetic packaging electrochemical techniques for neural stimulation applications novel electrode materials and testing thin lm exible microelectrode arrays in situ char terization of microelectrode arrays chip size thin lm device encapsulation microchip embedded capacitors and microelectronics for recording stimulation and wireless telemetry The design process in the development of medical devices is also discussed Advances in biomedical engineering microfabrication technology and neu science have led to improved medical device designs and novel functions However many challenges remain This book focuses on the engineering approaches R D advances and technical challenges of medical implants from an engineering p spective We are grateful to leading researchers from academic institutes national laboratories as well as design engineers and professionals from the medical device industry who have contributed to the book Part one of this series covers designs of implantable neural prosthetic devices and their clinical applications

Transparent Oxide Electronics Pedro Barquinha,Rodrigo Martins,Luis Pereira,Elvira Fortunato,2012-03-15 Transparent electronics is emerging as one of the most promising technologies for the next generation of electronic products away from the traditional silicon technology It is essential for touch display panels solar cells LEDs and antistatic coatings The book describes the concept of transparent electronics passive and active oxide semiconductors multicomponent dielectrics and their importance for a new era of novel electronic materials and products This is followed by a short history of transistors and how oxides have revolutionized this field It concludes with a glance at low cost disposable

and lightweight devices for the next generation of ergonomic and functional discrete devices Chapters cover Properties and applications of n type oxide semiconductors P type conductors and semiconductors including copper oxide and tin monoxide Low temperature processed dielectrics n and p type thin film transistors TFTs structure physics and brief history Paper electronics Paper transistors paper memories and paper batteries Applications of oxide TFTs transparent circuits active matrices for displays and biosensors Written by a team of renowned world experts Transparent Oxide Electronics From Materials to Devices gives an overview of the world of transparent electronics and showcases groundbreaking work on paper transistors *Ultra Clean Processing of Silicon Surfaces VII* Paul Mertens, Marc Meuris, Marc Heyns, 2005-04-01 UCPSS 2004 Proceedings of the 7th International Symposium on Ultra Cleyn Processing of Silicon Surfaces UCPSS Brussels Belgium Sept 20 22 2004 Fundamentals of Novel Oxide/semiconductor Interfaces C. R. Abernathy, 2004

Chemical-Mechanical Polishing - Fundamentals and Challenges: Volume 566 S. V. Babu, 2000-02-10 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners High Dielectric Constant Materials Howard Huff, 2005 Issues relating to the high K gate dielectric are among the greatest challenges for the evolving International Technology Roadmap for Semiconductors ITRS More than just an historical overview this book will assess previous and present approaches related to scaling the gate dielectric and their impact along with the creative directions and forthcoming challenges that will define the future of gate dielectric scaling technology Topics include an extensive review of Moore's Law the classical regime for SiO₂ gate dielectrics the transition to silicon oxynitride gate dielectrics the transition to high K gate dielectrics including the drive towards equivalent oxide thickness in the single digit nanometer regime and future directions and issues for ultimate technology generation scaling The vision wisdom and experience of the team of authors will make this book a timely relevant and interesting resource focusing on fundamentals of the 45 nm Technology Generation and beyond *High-k Gate Dielectrics for CMOS Technology* Gang He, Zhaoqi Sun, 2012-08-10 A state of the art overview of high k dielectric materials for advanced field effect transistors from both a fundamental and a technological viewpoint summarizing the latest research results and development solutions As such the book clearly discusses the advantages of these materials over conventional materials and also addresses the issues that accompany their integration into existing production technologies Aimed at academia and industry alike this monograph combines introductory parts for newcomers to the field as well as advanced sections with directly applicable solutions for experienced researchers and developers in materials science physics and electrical engineering **Handbook of Thin Film Materials: Ferroelectric and dielectric thin films** Hari Singh Nalwa, 2002 **Ultra Clean Processing of Silicon Surfaces ...**, 2004 **Materials for Smart Systems**, 1999 *JJAP*, 2009 Encyclopedia of Materials K. H. J. Buschow, 2001 Accompanying CR ROM contains The Encyclopedia of Materials Science and Technology on a web access disc

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