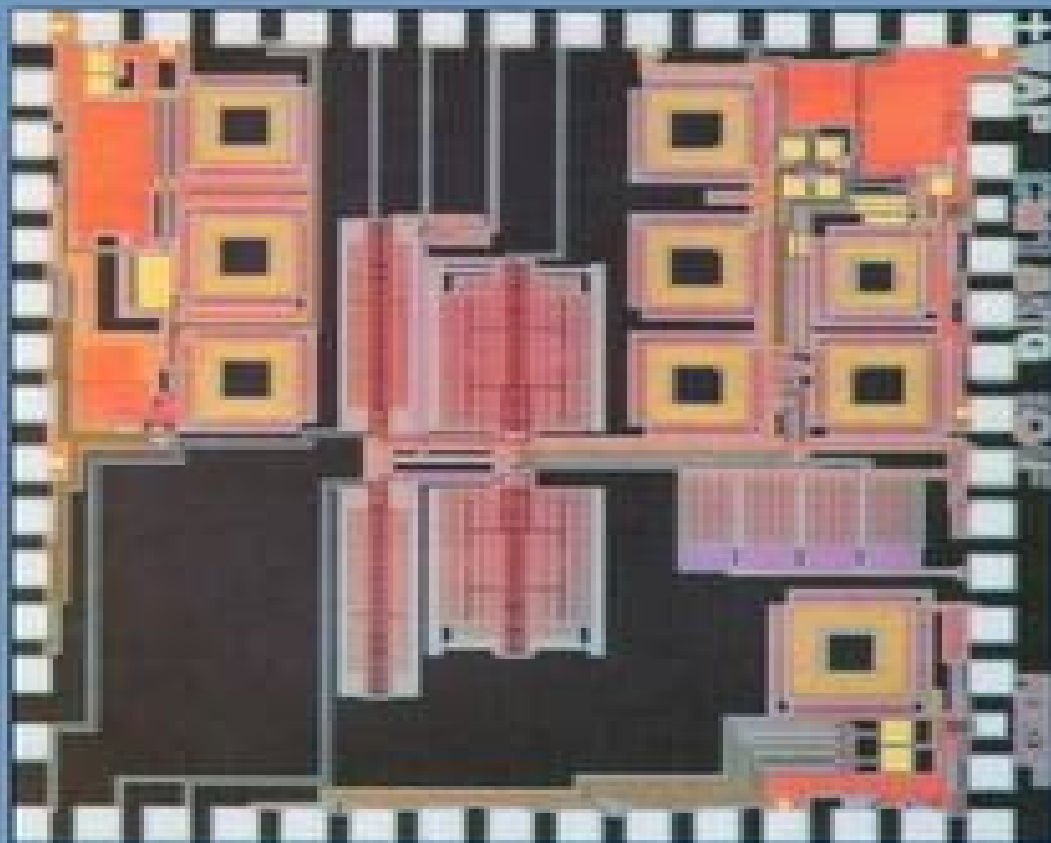


# Low-Power CMOS Design for Wireless Transceivers



Alireza Zolfaghari

Foreword by Behzad Razavi

# Lowpower Cmos Design For Wireless Transceivers

**Arthur James Wells**



## **Lowpower Cmos Design For Wireless Transceivers:**

Low-Power CMOS Design for Wireless Transceivers Alireza Zolfaghari, 2003 This comprehensive treatment of the challenges in low power RF CMOS design deals with the design and implementation of low power wireless transceivers in a standard digital CMOS process It addresses trade offs and techniques that improve performance from the component level to the architectural level

**Architectures and Synthesizers for Ultra-low Power Fast Frequency-Hopping WSN Radios** Emanuele Lopelli, Johan van der Tang, Arthur H.M. van Roermund, 2010-11-19 Wireless sensor networks have the potential to become the third wireless revolution after wireless voice networks in the 80s and wireless data networks in the late 90s Unfortunately radio power consumption is still a major bottleneck to the wide adoption of this technology Different directions have been explored to minimize the radio consumption but the major drawback of the proposed solutions is a reduced wireless link robustness The primary goal of Architectures and Synthesizers for Ultra low Power Fast Frequency Hopping WSN Radios is to discuss in detail existing and new architectural and circuit level solutions for ultra low power robust uni directional and bi directional radio links Architectures and Synthesizers for Ultra low Power Fast Frequency Hopping WSN Radios guides the reader through the many system circuit and technology trade offs he will be facing in the design of communication systems for wireless sensor networks Finally this book through different examples realized in both advanced CMOS and bipolar technologies opens a new path in the radio design showing how radio link robustness can be guaranteed by techniques that were previously exclusively used in radio systems for middle or high end applications like Bluetooth and military communications while still minimizing the overall system power consumption

*The Design of Low-Voltage, Low-Power Sigma-Delta Modulators* Shahriar Rabii, Bruce A. Wooley, 2012-12-06 Oversampling techniques based on sigma delta modulation are widely used to implement the analog digital interfaces in CMOS VLSI technologies This approach is relatively insensitive to imperfections in the manufacturing process and offers numerous advantages for the realization of high resolution analog to digital A D converters in the low voltage environment that is increasingly demanded by advanced VLSI technologies and by portable electronic systems In The Design of Low Voltage Low Power Sigma Delta Modulators an analysis of power dissipation in sigma delta modulators is presented and a low voltage implementation of a digital audio performance A D converter based on the results of this analysis is described Although significant power savings can typically be achieved in digital circuits by reducing the power supply voltage the power dissipation in analog circuits actually tends to increase with decreasing supply voltages Oversampling architectures are a potentially power efficient means of implementing high resolution A D converters because they reduce the number and complexity of the analog circuits in comparison with Nyquist rate converters In fact it is shown that the power dissipation of a sigma delta modulator can approach that of a single integrator with the resolution and bandwidth required for a given application In this research the influence of various parameters on the power dissipation of the modulator has been evaluated and strategies for the design of

a power efficient implementation have been identified The Design of Low Voltage Low Power Sigma Delta Modulators begins with an overview of A/D conversion emphasizing sigma delta modulators It includes a detailed analysis of noise in sigma delta modulators analyzes power dissipation in integrator circuits and addresses practical issues in the circuit design and testing of a high resolution modulator The Design of Low Voltage Low Power Sigma Delta Modulators will be of interest to practicing engineers and researchers in the areas of mixed signal and analog integrated circuit design **Low-power HF**

**Microelectronics** Gerson A. S. Machado, 1996 This book brings together innovative modelling simulation and design techniques in CMOS SOI GaAs and BJT to achieve successful high yield manufacture for low power high speed and reliable by design analogue and mixed mode integrated systems *Ultra Low Power Transceiver for Wireless Body Area Networks* Jens Masuch, Manuel Delgado-Restituto, 2013-03-28 Wireless Body Area Networks WBANs are expected to promote new applications for the ambulatory health monitoring of chronic patients and elderly population aiming to improve their quality of life and independence These networks are composed by wireless sensor nodes WSNs used for measuring physiological variables e.g. glucose level in blood or body temperature or controlling therapeutic devices e.g. implanted insulin pumps These nodes should exhibit a high degree of energy autonomy in order to extend their battery lifetime or even make the node supply to rely on harvesting techniques Typically the power budget of WSNs is dominated by the wireless link and hence many efforts have been directed during the last years toward the implementation of power efficient transceivers Because of the short range typically no more than a few meters and low data rate typically in between 10 kb/s and 1 Mb/s simple communication protocols can be employed One of these protocols specifically tailored for WBAN applications is the Bluetooth low energy BLE standard This book describes the challenges and solutions for the design of ultra low power transceivers for WBANs applications and presents the implementation details of a BLE transceiver prototype Coverage includes not only the main concepts and architectures for achieving low power consumption but also the details of the circuit design and its implementation in a standard CMOS technology **The Design of Low-power Integrated Radio-frequency Front-end in CMOS** Shaolei Quan, 2003 *Wearable Technologies: Concepts, Methodologies, Tools, and Applications* Management Association, Information Resources, 2018-04-06 Advances in technology continue to alter the ways in which we conduct our lives from the private sphere to how we interact with others in public As these innovations become more integrated into modern society their applications become increasingly relevant in various facets of life *Wearable Technologies Concepts Methodologies Tools and Applications* is a comprehensive reference source for the latest scholarly material on the development and implementation of wearables within various environments emphasizing the valuable resources offered by these advances Highlighting a range of pertinent topics such as assistive technologies data storage and health and fitness applications this multi volume book is ideally designed for researchers academics professionals students and practitioners interested in the emerging applications of wearable technologies *A Design Methodology for Highly-integrated Low-power*

*Receivers for Wireless Communications* Dennis Gee-Wai Yee, 2001      *Omnidirectional Inductive Powering for Biomedical Implants* Bert Lenaerts, Robert Puers, 2008-10-14 *Omnidirectional Inductive Powering for Biomedical Implants* investigates the feasibility of inductive powering for capsule endoscopy and freely moving systems in general. The main challenge is the random position and orientation of the power receiving system with respect to the emitting magnetic field. Where classic inductive powering assumes a predictable or fixed alignment of the respective coils, the remote system is now free to adopt just any orientation while still maintaining full power capabilities. Before elaborating on different approaches towards omnidirectional powering, the design and optimisation of a general inductive power link is discussed in all its aspects. Special attention is paid to the interaction of the inductive power link with the patient's body. Putting theory into practice, the implementation of an inductive power link for a capsule endoscope is included in a separate chapter.      **Ultra-low Power Wireless Technologies for Sensor Networks** Brian Otis, 2005      *Proceedings of the ... Biennial University/Government/Industry Microelectronics Symposium*,      **IEEE Transactions on Circuits and Systems**, 2006

Optoelectronics Engineering and Information Technologies in Industry D.A. Li, W.H. Zhou, 2013-09-18. Selected peer reviewed papers from the 2013 2nd International Conference on Opto Electronics Engineering and Materials Research OEMR 2013 October 19-20 2013 Zhengzhou Henan China      A Baseband, Impulse Ultra-wideband Transceiver Front-end for Low Power Applications Ian David O'Donnell, 2006. Interest in indoor wireless communications has been increasing. In addition to high throughput WLAN systems such as 802.11a/b/g/n, attention is also being focused on lower rate short distance systems such as Bluetooth and Zigbee. These low rate radios are being proposed for a variety of applications including automation, security, smart toys, remote sensing, control, asset tracking, and as a replacement for computer peripheral wires. While not demanding aggressive throughput, these radios do require low cost, power efficient operation, and optionally the ability to perform ranging. Unfortunately, currently reported radios are up to an order of magnitude away from these power and cost targets or do not support ranging. However, a recent ruling from the FCC has opened up nearly 8GHz of unlicensed spectrum from dc to 960MHz and from 3.1GHz to 10.6GHz for ultra wideband (UWB) deployment. One attractive method of UWB signaling that seems suited to a low power, highly integrated implementation communicates with short pulses on the order of a nanosecond that spread energy over at least 500MHz of bandwidth. Termed impulse UWB, the baseband nature of this signaling promises low cost and low power consumption through design simplicity, pulsed or duty cycled operation, and a mostly digital implementation. The benefits of this approach are balanced by the risk of jamming from in-band interference of stricter sampling and gain constraints and of increased digital complexity. This dissertation presents the system exploration, specification, design, and demonstration of a low power, highly integrated, flexible baseband impulse ultra wideband transceiver front end.      Tsinghua Science and Technology, 2003      *Integrated Circuits for Wireless Communications* Asad A. Abidi, Paul R. Gray, Robert G. Meyer, 1998-12-24. *Electrical Engineering: Integrated Circuits for Wireless Communications*

High frequency integrated circuit design is a booming area of growth that is driven not only by the expanding capabilities of underlying circuit technologies like CMOS but also by the dramatic increase in wireless communications products that depend on them. Integrated Circuits for Wireless Communications includes seminal and classic papers in the field and is the first all in one resource to address this increasingly important topic. Internationally known and highly regarded in the field, editors Asad Abidi, Paul Gray, and Robert G Meyer have meticulously compiled more than 100 papers and articles covering the very latest high level integrated circuits techniques and solutions in use today. Integrated Circuits for Wireless Communications is devised expressly to provide IC design engineers, system architects, and integrators with a practical understanding of subjects ranging from architecture choices for integrated transceivers to actual circuit designs in all viable IC technologies such as bipolar, CMOS, and GaAs. The papers selected represent a breadth of coverage and level of expertise that is simply unmatched in the field. Topics covered include: Radio architectures, Receivers, Transmitters, and transceivers, Power amplifiers, and RF switches, Oscillators, Passive components, Systems applications.

**The British National**  
**Bibliography** Arthur James Wells, 2003      **Subject Guide to Books in Print**, 1991      1994 IEEE Symposium on Low  
Power Electronics John H. Wuorinen, 1994      *Proceedings 2001*, 2001

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