



Low-Voltage, Low-Power Digital BiCMOS Circuits

Circuit Design, Comparative Study and Sensitivity Analysis

- BiCMOS design for today's fastest-growing applications
- Design concepts, in-depth analyses, and new methodologies
- Choosing the best BiCMOS circuit for your application
- Predicting and managing circuit performance sensitivities

SAMIR S. ROFAIL & KIAT-SENG YEO

Low Voltage Low Power Digital Bicmos Circuits Circuit Design Comparative Study And Sensitivity

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Low Voltage Low Power Digital Bicmos Circuits Circuit Design Comparative Study And Sensitivity:

Low-voltage, Low-power Digital BiCMOS Circuits Samir S. Rofail,Kiat Seng Yeo,2000 PLEASE PROVIDE COURSE INFORMATION PLEASE PROVIDE *Signal Integrity Issues and Printed Circuit Board Design* Douglas Brooks,2003 Complicated concepts explained succinctly and in laymen s terms to both experienced and novice PCB designers Numerous examples allow reader to visualize how high end software simulators see various types of SI problems and then their solutions Author is a frequent and recognized seminar leader in the industry **Essential Electronic Design Automation (EDA)** Mark Birnbaum,2004 Describes the engineering needs addressed by the individual EDA tools and covers EDA from both the provider and user viewpoints Learn the importance of marketing and business trends in the EDA industry The EDA consortium is made up of major corporations including SUN HP and Intel *Printed Circuit Board Designer's Reference* Christopher T. Robertson,2004 PCB design instruction and reference manual all in one book with in depth explanation of the processes and tools used in modern PCB design Standards formulas definitions and procedures plus software to tie it all together *Signal Integrity* Eric Bogatin,2004 This thorough review of the fundamental principles associated with signal integrity provides engineering principles behind signal integrity effects and applies this understanding to solving problems

High-speed Signal Propagation Howard W. Johnson,Martin Graham,2003 This advanced level reference presents a complete and unified theory of signal propagation for all metallic media from cables to pcb traces to chips It includes numerous examples pictures tables and wide ranging discussion of the high speed properties of transmission lines *From ASICs to SOCs* Farzad Nekoogar,Faranak Nekoogar,2003 From ASICs to SOCs A Practical Approach by Farzad Nekoogar and Faranak Nekoogar covers the techniques principles and everyday realities of designing ASICs and SOCs Material includes current issues in the field front end and back end designs integration of IPs on SOC designs and low power design techniques and methodologies Appropriate for practicing chip designers as well as graduate students in electrical engineering

CMOS/BiCMOS ULSI Kiat Seng Yeo, Samir S. Rofail,Wang-Ling Goh,2002 For upper level and graduate level Electrical and Computer Engineering courses in Integrated Circuit Design as well as professional circuit designers engineers and researchers working in portable wireless communications hardware This book presents the fundamentals of Complementary Metal Oxide Semiconductor CMOS and Bipolar compatible Complementary Metal Oxide Semiconductor BiCMOS technology as well as the latest technological advances in the field It discusses the concepts and techniques of new integrated circuit design for building high performance and low power circuits and systems for current and future very large scale integration VLSI and giga scale integration GSI applications CMOS BiCMOS ULSI Low Voltage Low Power is an essential resource for every professional moving toward lower voltage lower power and higher performance VLSI circuits and subsystems design

American Book Publishing Record ,1999 *High-Performance Digital VLSI Circuit Design* Richard X. Gu,Khaled M. Sharaf,Mohamed I. Elmasry,1996 High Performance Digital VLSI Circuit Design is devoted to the analysis and design of

digital VLSI CMOS bipolar and BiCMOS circuits which are optimized for high performance applications The book starts by reviewing important background information in the area of MOS and bipolar device design and modeling Detailed analysis and design of high performance CMOS CML ECL NTL and BiCMOS circuits is given Achieving high speed while maintaining low power dissipation in digital circuits is addressed in depth in separate chapters The book ends with a sample application area of high performance design namely the design of phase locked loops The book can be used as a reference for practicing IC designers and as a text for graduate and senior undergraduate students in the area of digital IC design **IEEE**

Transactions on Circuits and Systems ,2006 **Electrical & Electronics Abstracts** ,1997 *Scientific and Technical Aerospace Reports* ,1991 BiCMOS Integrated Circuit Design IEEE Solid-State Circuits Council,1994 This up to date book with its introductory tutorial provides extensive coverage on BiCMOS potentially the most important silicon technology of the 1990 s Many commercial products are currently utilizing BiCMOS ICs and an exponential growth is expected Elmasry

provides the latest information on processing technologies circuit analysis and techniques and applications in the areas of analog digital and smart power BiCMOS Integrated Circuit Design is an important guide for engineers working in BiCMOS processing modeling characterization circuit design and applications as well as a valuable learning tool for college seniors and graduate students Applied Science & Technology Index ,1996 International Symposium on Quality Electronic Design ,2002 Annotation Fifty one papers and 21 posters from the March 2002 symposium report current research in deep submicron integrated circuit design and development The sessions address interconnect extraction and modeling design for process variations metrics power and noise management verification signal integrity and low power design techniques Some of the topics are transition aware global signaling TAGS the interoperability of EDA tools for sequential logic synthesis statistical methods for the determination of process corners power supply noise suppression via clock skew scheduling and the relation between SAT and BDDs for equivalence checking No subject index Annotation copyrighted by Book News Inc

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