
THE MIPS-X RISC MICROPROCESSOR

edited by

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University of Toronto



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Mips X Risc Microprocessor

Bank W. Lee, Bing J. Sheu

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The MIPS-X RISC Microprocessor Paul Chow,2013-03-09 The first Stanford MIPS project started as a special graduate course in 1981 That project produced working silicon in 1983 and a prototype for running small programs in early 1984 After that we declared it a success and decided to move on to the next project MIPS X This book is the final and complete word on MIPS X The initial design of MIPS X was formulated in 1984 beginning in the Spring At that time we were unsure that RISe technology was going to have the industrial impact that we felt it should We also knew of a number of architectural and implementation flaws in the Stanford MIPS machine We believed that a new processor could achieve a performance level of over 10 times a VAX 11 780 and that a microprocessor of this performance level would convince academic skeptics of the value of the RISe approach We were concerned that the flaws in the original RISe design might overshadow the core ideas or that attempts to industrialize the technology would repeat the mistakes of the first generation designs MIPS X was targeted to eliminate the flaws in the first generation de signs and to boost the performance level by over a factor of five

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and AMD 29000 RISC microprocessor family This book is a valuable resource for consumers interested in RISC microprocessors Field-programmable Logic and Applications ,1994 **A CRegs Implementation Study Based on the MIPS-X RISC Processor** Steve Nowakowski,Matthew T. O'Keefe,Army High Performance Computing Research Center,1991

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David Anthony Greene,2003 **RISC Architectures** J.C. Heudin,C. Panetto,1992-05-31 This book covers the principles and techniques that make RISC a unique and elegant approach for high performance microprocessor system designs describing both the practical and theoretical aspects of the RISC design philosophy **Hardware Annealing in Analog VLSI**

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