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Multiprocessor Performance

**Kang G. Shin, Michael H.
Woodbury, Yann-Hang Lee**

Multiprocessor Performance:

Multiprocessor Performance Debugging and Memory Bottlenecks A. J. Goldberg,1992 **Multiprocessor Performance Measurement and Evaluation** Laxmi N. Bhuyan,Xiaodong Zhang,1995 **Multiprocessor Performance** Erol Gelenbe,1989 The first book devoted to the fundamental issues involved in the performance of parallel computers Multiprocessor Performance describes the architecture of such systems identifies the factors that enhance or limit their speed addresses problems related to the structure of application programs and shows how to determine the degree of performance improvement that can be obtained via parallelism Examines the limiting effects of program structure via study of program behavior models **Using Compression to Improve Chip Multiprocessor Performance** Alaa R. Alameldeen,2006

Performance Modeling for Computer Architects C. M. Krishna,1995-10-14 As computers become more complex the number and complexity of the tasks facing the computer architect have increased Computer performance often depends in complex way on the design parameters and intuition that must be supplemented by performance studies to enhance design productivity This book introduces computer architects to computer system performance models and shows how they are relatively simple inexpensive to implement and sufficiently accurate for most purposes It discusses the development of performance models based on queuing theory and probability The text also shows how they are used to provide quick approximate calculations to indicate basic performance tradeoffs and narrow the range of parameters to consider when determining system configurations It illustrates how performance models can demonstrate how a memory system is to be configured what the cache structure should be and what incremental changes in cache size can have on the miss rate A particularly deep knowledge of probability theory or any other mathematical field to understand the papers in this volume is not required

The Cache Coherence Problem in Shared-Memory Multiprocessors Igor Tortalja, Veljko Milutinović,1996-02-13 Almost all software solutions are developed through academic research and implemented only in prototype machines leaving the field of software techniques for maintaining the cache coherence widely open for future research and development This book is a collection of all the representative approaches to software coherence maintenance including a number of related efforts in the performance evaluation field The book presents a selection of 27 papers dealing with state of the art software solutions for cache coherence maintenance in shared memory multiprocessors It begins with a set of four introductory readings that provides a brief overview of the cache coherence problem and introduces software solutions to the problem The text defines and illustrates static and dynamic software schemes techniques for modeling performance evaluation mechanisms and performance evaluation studies The book is intended for the experienced reader in computer engineering but possibly a novice in the topic of cache coherence It also provides an in depth understanding of the problem as well as a comprehensive overview for multicomputer designers computer architects and compiler writers In addition it is a software coherence reference handbook for advanced undergraduate and typical graduate students in

multiprocessing and multiprogramming areas
Marsan,Gianfranco Balbo, Gianni Conte,1990
abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA Scientific and Technical Information Database

Performance Models of Multiprocessor Systems M. Ajmone
Scientific and Technical Aerospace Reports ,1991 Lists citations with
Proceedings of the USENIX Symposium on Experiences with Distributed and Multiprocessor Systems (SEDMS IV) USENIX Association,1993
Publications of the National Institute of Standards and Technology ... Catalog National Institute of Standards and Technology (U.S.),1991
Modelling Techniques and Tools for Performance Analysis D. Potier,1985 Hardbound This proceedings volume contains the thirty five invited and selected papers presented at the International Conference on Modelling Techniques and Tools for Performance Analysis The conference had two main objectives to promote the most recent advances in the field of modelling techniques and tools and to present a spectrum of new specialized products that are now reaching industrial and technical maturity The technical content of the proceedings is specifically oriented towards practice and experience with techniques and tools

Speedup Prediction and Diagnosis for Shared Memory Multiprocessor Systems Thin Fong Tsuei,1990
Multiprocessor System Architectures Ben J. Catanzaro,1994 Provides an overview of SPARC architecture including architecture conformance semi conductor technology scalability multiprocessor support as well as system level resources SPARC multi level Bus architectures MBus and XBus multiprocessor system design and simulation and multiprocessor software Geared to engineers and engineering professionals who want to understand the various architectural components both hardware and software from Sun Microsystems

Image Processing Algorithms and Techniques II Mehmet Reha Civanlar, Sanjit Kumar Mitra, Robert J. Moorhead, 1991
Modeling and Measurement of Fault-tolerant Multiprocessors Kang G. Shin, Michael H. Woodbury, Yann-Hang Lee, 1985

Multiprocessors M. Satyanarayanan, 1980 Gives the reader an idea of how multiprocessing is done with some of the computer computers systems found today with eight multiprocessors as case studies

1987 Winter Simulation Conference Proceedings Arne Thesen, Hank Grant, W. David Kelton, 1987

Resources in Parallel and Concurrent Systems ,1991 Computer Systems Organization Parallel architecture

A Conflict-free Memory Design for Multiprocessors Honda Shing, 1992

SEDMS ,1993

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