

Computing Multiple-Valued Logic Programs *

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Abstract

The logic of signed formulas can be used to reason about a wide variety of multiple-valued logics [Häh94b, LMR97]. The formal theoretical foundation of multiple-valued logic programming based on signed formulas is set forth in [Lu96]. The current paper is an investigation into the operational semantics of such signed logic programming. The connection of signed logic programming to constraint logic programming is presented, search space issues are briefly discussed for both general and special cases, and applications to bilattice logic programming and truth-maintenance are analyzed.

Keywords: Logic for Artificial Intelligence, Multiple-valued Logic, Signed Formula, Constraint Logic Programming, Truth-Maintenance, Bilattices.

1. Introduction

The logic of signed formulas facilitates the examination of questions regarding multiple-valued logics through classical logic. As such, logic programming based on signed formulas also facilitates the analysis of multiple-valued logic programming systems through classical logic programming. The theoretical foundation and the applications of the logic of signed formulas have been investigated extensively [Häh91, Häh94a, LMR97, MR94]. On the other hand, logic programming based on signed formulas — signed formula logic programming — is only formalized recently [Lu96]. There, the semantical connections between a signed formula logic program and its associated underlying multiple-valued logic program are studied. In addition, the relationships between signed formula logic programming and the class of annotated logic programming [BS89, KS92] are established. It is shown

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International Symposium on Multiple Valued Logic (34, 2004, Toronto), IEEE Computer Society, 2004 *Multiple-Valued Logic* Institute of Electrical and Electronics Engineers, Inc. Staff, 1999 This text covers the 29th International Conference on Multi Valued Logic looking at computational mathematics and complexity theory It is aimed at researchers professors practitioners students and other computing professionals **Multiple Valued Logic** IEEE Staff, 1988-05 *Twelfth International Symposium on Multiple-valued Logic*, 1982 **Modern Uses of Multiple-Valued Logic** M. Dunn, G. Epstein, 2012-12-06 This is a collection of invited papers from the 1975 International Symposium on Multiple valued Logic Also included is an extensive bibliography of works in the field of multiple valued logic prior to 1975 this supplements and extends an earlier bibliography of works prior to 1965 by Nicholas Rescher in his book Many Valued Logic McGraw Hill 1969 There are a number of possible reasons for interest in the present volume First the range of various uses covered in this collection of papers may be taken as indicative of a breadth which occurs in the field of multiple valued logic as a whole the papers here can do no more than cover a small sample question answering systems analysis of computer hazards algebraic structures relating to multiple valued logic algebra of computer programs fuzzy sets Second a large part of the interest in such uses and applications has occurred in the last twenty even ten years It would be too much to expect this to be reflected in Rescher's 1969 book Third in the 1970's a series of annual symposia have been held on multiple valued logic which have brought much of this into a sharp focus The 1971 and 1972 symposia were held at the SUNY at Buffalo the 1973 symposium at the University of Toronto and the 1974 symposium at West Virginia University Papers from these symposia are included in the bibliography which may be found in an appendix of this book **Multiple Valued Logic** IEEE Computer Society Press, 1987-10 *18th International Symposium on Multiple-valued Logic*, 1988 **Proceedings of the Sixth International Symposium on Multiple-valued Logic**, 1976 **Proceedings of the Eight International Symposium on Multiple-valued Logic** S. C. Crist, 1978 Proceedings, 1996 Proceedings, 2001 *Histoire de la révolution de France, par deux amis de la liberté*, 1792 **International Symposium on Multiple-Valued Logic, 6th. Proceedings** Institute of Electrical and Electronics Engineers (New York), University of Utah, Congrès. Logan. Utah. 1976, 1976 *Proceedings of the International Symposium on Multiple-valued Logic*, 1976 Proceedings, 1978 **Proceedings** International Symposium on Multiple-Valued Logic, 1993 **Proceedings of the International Symposium on Multiple-Valued Logic (8th)**. Anthony S. Wojcik, Robert Swartwout, ILLINOIS INST OF TECH CHICAGO DEPT OF COMPUTER SCIENCE., 1978

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