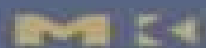# OPTIMIZING COMPILERS for MODERN ARCHITECTURES

Randy Allen & Ken Kennedy

Optimizing Compilers For Modern Architectures A Dependencebased Approach

Ayse Betul Buyukkurt

Optimizing Compilers For Modern Architectures A Dependencebased Approach:

Optimizing Compilers for Modern Architectures: A Dependence-Based Approach Randy Allen,Ken Kennedy,2001-10 Modern computer architectures designed with high performance microprocessors offer tremendous potential gains in performance over previous designs Yet their very complexity makes it increasingly difficult to produce efficient code and to realize their full potential This landmark text from two leaders in the field focuses on the pivotal role that compilers can play in addressing this critical issue The basis for all the methods presented in this book is data dependence a fundamental compiler analysis tool for optimizing programs on high performance microprocessors and parallel architectures It enables compiler designers to write compilers that automatically transform simple sequential programs into forms that can exploit special features of these modern architectures The text provides a broad introduction to data dependence to the many transformation strategies it supports and to its applications to important optimization problems such as parallelization compiler memory hierarchy management and instruction scheduling The authors demonstrate the importance and wide applicability of dependence based compiler optimizations and give the compiler writer the basics needed to understand and implement them They also offer cookbook explanations for transforming applications by hand to computational scientists and engineers who are driven to obtain the best possible performance of their complex applications The approaches presented are based on research conducted over the past two decades emphasizing the strategies implemented in research prototypes at Rice University and in several associated commercial systems Randy Allen and Ken Kennedy have provided an indispensable resource for researchers practicing professionals and graduate students engaged in designing and optimizing compilers for modern computer architectures Offers a guide to the simple practical algorithms and approaches that are most effective in real world high performance microprocessor and parallel systems Demonstrates each transformation in worked examples Examines how two case study compilers implement the theories and practices described in each chapter Presents the most complete treatment of memory hierarchy issues of any compiler text Illustrates ordering relationships with dependence graphs throughout the book Applies the techniques to a variety of languages including Fortran 77 C hardware definition languages Fortran 90 and High Performance Fortran Provides extensive references to the most sophisticated algorithms known in research *The Compiler Design Handbook* Y.N. Srikant,Priti Shankar,2018-10-03 Today s embedded devices and sensor networks are becoming more and more sophisticated requiring more efficient and highly flexible compilers Engineers are discovering that many of the compilers in use today are ill suited to meet the demands of more advanced computer architectures Updated to include the latest techniques The Compiler Design Handbook Second Edition offers a unique opportunity for designers and researchers to update their knowledge refine their skills and prepare for emerging innovations The completely revised handbook includes 14 new chapters addressing topics such as worst case execution time estimation garbage collection and energy aware compilation The editors take special care to consider the

growing proliferation of embedded devices as well as the need for efficient techniques to debug faulty code New contributors provide additional insight to chapters on register allocation software pipelining instruction scheduling and type systems Written by top researchers and designers from around the world The Compiler Design Handbook Second Edition gives designers the opportunity to incorporate and develop innovative techniques for optimization and code generation

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