

Nominal Size (Ton)	15	17.5	20	25	
				ECC	GCC
Cooling Performance^(a)					
Gross Cooling Capacity	194,000	214,000	252,000	280,000	280,000
AHRI Net Cooling Capacity	180,000	206,000	244,000	268,000	268,000
Nominal Airflow CFM / AHRI Rated CFM	5,400	6,650	7,000	8,000	8,000
EER	11	11	10	10	9.8
IEER (Multi -Speed Fan)	14.2	14.2	13.2	13.2	13.2
Gas Heating Performance^(b)					
Low Heat (Input/Output) - (MBh)	240,0/194,0	240,0/194,0	240,0/194,0	-	240,0/194,0
Medium Heat (Input/Output) - (MBh)	320,0/259,0	320,0/259,0	320,0/259,0	-	320,0/259,0
High Heat (Input/Output) - (MBh)	350,0/284,0	380 (Downflow), 350 (Horizontal)/ 308 (Downflow), 284 (Horizontal) (c)	380,0/308,0	-	380,0/308,0
Other Information					
Net Weight (Lbs) - Gas Heat	2013	2028	2025	-	2061
Net Weight (Lbs) - Electric Heat	1850	1855	1860	1896	-
Filters - Type Furnished	Throwaway	Throwaway	Throwaway	Throwaway	Throwaway
Number and Size Recommended	(8) 20x25x2	(8) 20x25x2	(8) 20x25x2	(8) 20x25x2	(8) 20x25x2

Packaged Performance

F Rizvi



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Microengineering Aerospace Systems Henry Helvajian, 1999 Microengineering Aerospace Systems is a textbook tutorial encompassing MEMS micro electromechanical systems nanoelectronics packaging processing and materials characterization for developing miniaturized smart instruments for aerospace systems i e ASIM application specific integrated microinstrument satellites and satellite subsystems Third in a series of Aerospace Press publications covering this rapidly advancing technology this work presents fundamental aspects of the technology and specific aerospace systems applications through worked examples Computerworld ,1998-06-01 For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld s award winning Web site Computerworld com twice monthly publication focused conference series and custom research form the hub of the world s largest global IT media network **System-level Modeling of MEMS** Oliver Brand, Gary K. Fedder, Christofer Hierold, Jan G. Korvink, Osamu Tabata, 2012-12-20 System level modeling of MEMS microelectromechanical systems comprises integrated approaches to simulate understand and optimize the performance of sensors actuators and microsystems taking into account the intricacies of the interplay between mechanical and electrical properties circuitry packaging and design considerations Thereby system level modeling overcomes the limitations inherent to methods that focus only on one of these aspects and do not incorporate their mutual dependencies The book addresses the two most important approaches of system level modeling namely physics based modeling with lumped elements and mathematical modeling employing model order reduction methods with an emphasis on combining single device models to entire systems At a clearly understandable and sufficiently detailed level the readers are made familiar with the physical and mathematical underpinnings of MEMS modeling This enables them to choose the adequate methods for the respective application needs This work is an invaluable resource for all materials scientists electrical engineers scientists working in the semiconductor and or sensor industry physicists and physical chemists *Advanced Electronic Packaging Materials: Constitutive Model, Simulation, Design and Reliability* Yutai Su, Xu Long, Chuantong Chen, Xujiang Chao, Bo Wan, 2025-11-14 In the swiftly progressing landscape of the electronics industry the investigation of advanced electronic packaging materials stands as an essential frontier for technological breakthroughs This field is committed to substantially enhancing the performance functionality and durability of packaging materials while precisely predicting and controlling their behavior through cutting edge constitutive models multi physics simulations innovative design methodologies and thorough reliability assessments These endeavors offer deep insights into the utilization of groundbreaking materials and approaches underscoring their vital role in shaping the future technological infrastructure The aim of this research is not only to revolutionize the electronics industry by developing materials that excel under extreme conditions and diverse operational environments but also to push the boundaries of traditional packaging with materials such as sintered nanoparticles lead free solders composite materials underfill materials and third generation semiconductor

materials This initiative seeks to engineer state of the art electronic packaging solutions that are robust lightweight and capable of high thermal and electrical performance which are pivotal for devices exposed to intense stress or extreme temperatures By overcoming current limitations in electronic packaging design and functionality and incorporating advanced structures such as high density bonding wires Through Silicon Vias TSVs large scale adhesion layers micro bumps Wafer Level Chip Scale Packaging WLCSP and Fan Out Wafer Level Packaging FOWLP this field of study is on the brink of ushering in a new era of electronics These technologies are set to integrate more seamlessly into a vast array of applications dramatically enhancing performance and fostering new technological capabilities with improved reliability and efficiency For researchers and scientists dedicated to the field of advanced electronic packaging materials this Research Topic provides an ideal platform to share their latest developments and insights Advances in materials science creative design strategies and the integration of these innovations into next generation electronics have the potential to transform pivotal sectors including three dimensional packaging heterogeneous integrations Micro Electro Mechanical Systems MEMS Wide Bandgap WBG Semiconductors telecommunications power electronics automotive technology and space exploration The topics covered under this research scope include but are not limited to 1 Advanced characterization and development of electronic packaging materials 2 Novel constitutive models of electronic packaging materials 3 Manufacturing and process optimization for advanced electronic packaging 4 Thermal management in advanced electronic packaging 5 Multi physics simulations for advanced electronic packaging 6 Structural design in advanced electronic packaging 7 Reliability testing and failure analysis in advanced electronic packaging 8 Reliability assessment for advanced electronic packaging

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Current Trends in Protective Packaging of Computers and Electronic Components Robert J. Peache, 1988

"Code of Massachusetts regulations, 2001", 2001

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