

LOW POWER VLSI DESIGN AND TECHNOLOGY

Editors

G. K. Yeap
F. N. Najm



Low Power Vlsi Design And Technology

Ajit Pal

Low Power Vlsi Design And Technology:

Low Power VLSI Design and Technology Gary K. Yeap, Farid N. Najm, 1996 Low power and low energy VLSI has become an important issue in today's consumer electronics. This book is a collection of pioneering applied research papers in low power VLSI design and technology. A comprehensive introductory chapter presents the current status of the industry and academic research in the area of low power VLSI design and technology. Other topics cover logic synthesis, floorplanning, circuit design and analysis from the perspective of low power requirements. The readers will have a sampling of some key problems in this area as the low power solutions span the entire spectrum of the design process. The book also provides excellent references on up to date research and development issues with practical solution techniques. *Low-Power VLSI Circuits and Systems* Ajit Pal, 2014-11-17 The book provides a comprehensive coverage of different aspects of low power circuit synthesis at various levels of design hierarchy starting from the layout level to the system level. For a seamless understanding of the subject basics of MOS circuits has been introduced at transistor, gate and circuit level followed by various low power design methodologies such as supply voltage scaling, switched capacitance minimization techniques and leakage power minimization approaches. The content of this book will prove useful to students, researchers as well as practicing engineers.

Practical Low Power Digital VLSI Design Gary K. Yeap, 2012-12-06 Practical Low Power Digital VLSI Design emphasizes the optimization and trade off techniques that involve power dissipation in the hope that the readers are better prepared the next time they are presented with a low power design problem. The book highlights the basic principles, methodologies and techniques that are common to most CMOS digital designs. The advantages and disadvantages of a particular low power technique are discussed. Besides the classical area performance trade off, the impact to design cycle time, complexity, risk, testability and reusability are discussed. The wide impacts to all aspects of design are what make low power problems challenging and interesting. Heavy emphasis is given to top down structured design style with occasional coverage in the semicustom design methodology. The examples and design techniques cited have been known to be applied to production scale designs or laboratory settings. The goal of Practical Low Power Digital VLSI Design is to permit the readers to practice the low power techniques using current generation design style and process technology. Practical Low Power Digital VLSI Design considers a wide range of design abstraction levels spanning circuit, logic, architecture and system. Substantial basic knowledge is provided for qualitative and quantitative analysis at the different design abstraction levels. Low power techniques are presented at the circuit, logic, architecture and system levels. Special techniques that are specific to some key areas of digital chip design are discussed as well as some of the low power techniques that are just appearing on the horizon. Practical Low Power Digital VLSI Design will be of benefit to VLSI design engineers and students who have a fundamental knowledge of CMOS digital design. *The Electrical Engineering Handbook* Wai Kai Chen, 2004-11-16 The Electrical Engineer's Handbook is an invaluable reference source for all practicing electrical engineers and students.

Encompassing 79 chapters this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students. This text will most likely be the engineer's first choice in looking for a solution. Extensive complete references to other sources are provided throughout. No other book has the breadth and depth of coverage available here. This is a must have for all practitioners and students. The Electrical Engineer's Handbook provides the most up to date information in Circuits and Networks, Electric Power Systems, Electronics, Computer Aided Design and Optimization, VLSI Systems, Signal Processing, Digital Systems and Computer Engineering, Digital Communication and Communication Networks, Electromagnetics and Control and Systems. About the Editor in Chief: Wai Kai Chen is Professor and Head Emeritus of the Department of Electrical Engineering and Computer Science at the University of Illinois at Chicago. He has extensive experience in education and industry and is very active professionally in the fields of circuits and systems. He was Editor in Chief of the IEEE Transactions on Circuits and Systems Series I and II, President of the IEEE Circuits and Systems Society, and is the Founding Editor and Editor in Chief of the Journal of Circuits, Systems and Computers. He is the recipient of the Golden Jubilee Medal, the Education Award and the Meritorious Service Award from the IEEE Circuits and Systems Society, and the Third Millennium Medal from the IEEE. Professor Chen is a fellow of the IEEE and the American Association for the Advancement of Science. 77 chapters encompass the entire field of electrical engineering. THOUSANDS of valuable figures, tables, formulas and definitions. Extensive bibliographic references.

Low Power VLSI Design

Angsuman Sarkar, Swapnajit De, Manash Chanda, Chandan Kumar Sarkar, 2016-08-08. This book teaches basic and advanced concepts, new methodologies and recent developments in VLSI technology with a focus on low power design. It provides insight on how to use Tanner Spice, Cadence tools, Xilinx tools, VHDL programming and Synopsis to design simple and complex circuits using the latest state of the art technologies. Emphasis is placed on fundamental transistor circuit level design concepts.

Signal Processing, Telecommunication & Embedded Systems: AI and ML Applications

Vikrant Bhateja, Zaid Omar, Anumoy Ghosh, Sarika Shrivastava, 2025-11-01. The book discusses the latest developments and outlines future trends in the fields of microelectronics, electromagnetics and telecommunication. It contains original research works presented at the International Conference on Microelectronics, Electromagnetics and Telecommunication (ICMEET 2024) organized by the Department of Electronics and Communication Engineering, National Institute of Technology, Mizoram, India, during 19-20 December 2024. The book is divided into four volumes and it covers papers written by scientists, research scholars, and practitioners from leading universities, engineering colleges, and R&D institutes from all over the world and shares the latest breakthroughs in and promising solutions to the most important issues facing today's society.

Smart Computing

Mohammad Ayoub Khan, Sanjay Gairola, Bhola Jha, Pushkar Praveen, 2021-06-22. The field of SMART technologies is an interdependent discipline. It involves the latest burning issues ranging from machine learning, cloud computing, optimisations, modelling techniques, Internet of Things, data analytics, and Smart Grids among others that are all new fields. It is an applied

and multi disciplinary subject with a focus on Specific Measurable Achievable Realistic Timely system operations combined with Machine intelligence Real Time computing It is not possible for any one person to comprehensively cover all aspects relevant to SMART Computing in a limited extent work Therefore these conference proceedings address various issues through the deliberations by distinguished Professors and researchers The SMARTCOM 2020 proceedings contain tracks dedicated to different areas of smart technologies such as Smart System and Future Internet Machine Intelligence and Data Science Real Time and VLSI Systems Communication and Automation Systems The proceedings can be used as an advanced reference for research and for courses in smart technologies taught at graduate level 'Advances in Microelectronics: Reviews', Vol_1 Sergey Yurish,2017-12-24 The 1st volume of Advances in Microelectronics Reviews Book Series contains 19 chapters written by 72 authors from academia and industry from 16 countries With unique combination of information in each volume the Advances in Microelectronics Reviews Book Series will be of value for scientists and engineers in industry and at universities In order to offer a fast and easy reading of the state of the art of each topic every chapter in this book is independent and self contained All chapters have the same structure first an introduction to specific topic under study second particular field description including sensing applications Each of chapter is ending by well selected list of references with books journals conference proceedings and web sites This book ensures that readers will stay at the cutting edge of the field and get the right and effective start point and road map for the further researches and developments Low-Power Digital VLSI Design Abdellatif Bellaouar,Mohamed Elmasry,2012-12-06 Low Power Digital VLSI Design Circuits and Systems addresses both process technologies and device modeling Power dissipation in CMOS circuits several practical circuit examples and low power techniques are discussed Low voltage issues for digital CMOS and BiCMOS circuits are emphasized The book also provides an extensive study of advanced CMOS subsystem design A low power design methodology is presented with various power minimization techniques at the circuit logic architecture and algorithm levels Features Low voltage CMOS device modeling technology files design rules Switching activity concept low power guidelines to engineering practice Pass transistor logic families Power dissipation of I O circuits Multi and low VT CMOS logic static power reduction circuit techniques State of the art design of low voltage BiCMOS and CMOS circuits Low power techniques in CMOS SRAMs and DRAMs Low power on chip voltage down converter design Numerous advanced CMOS subsystems e g adders multipliers data path memories regular structures phase locked loops with several design options trading power delay and area Low power design methodology power estimation techniques Power reduction techniques at the logic architecture and algorithm levels More than 190 circuits explained at the transistor level Software Radio Jeffrey Hugh Reed,2002 This guide to radio engineering covers every technique DSP and RF engineers need to build software radios for a wide variety of wireless systems using DSP techniques Included are practical guidelines for choosing DSP microprocessors and systematic object oriented software design techniques **Proceedings of Third International Conference on Computational**

Electronics for Wireless Communications Sanyog Rawat,Arvind Kumar,Ashish Raman,Sandeep Kumar,Parul Pathak,2024-10-19 This book includes high quality papers presented at Third International Conference on Computational Electronics for Wireless Communications ICCWC 2023 held at Dr B R Ambedkar National Institute of Technology Jalandhar India during October 20 21 2023 The book presents original research work of academics and industry professionals to exchange their knowledge of the state of the art research and development in computational electronics with an emphasis on wireless communications The topics covered in the book are radio frequency and microwave signal processing microelectronics and wireless networks Logic Synthesis for Low Power VLSI Designs Sasan Iman,Massoud Pedram,1998 Logic Synthesis for Low Power VLSI Designs presents a systematic and comprehensive treatment of power modeling and optimization at the logic level More precisely this book provides a detailed presentation of methodologies algorithms and CAD tools for power modeling estimation and analysis synthesis and optimization at the logic level Logic Synthesis for Low Power VLSI Designs contains detailed descriptions of technology dependent logic transformations and optimizations technology decomposition and mapping and post mapping structural optimization techniques for low power It also emphasizes the trade off techniques for two level and multi level logic circuits that involve power dissipation and circuit speed in the hope that the readers can better understand the issues and ways of achieving their power dissipation goal while meeting the timing constraints Logic Synthesis for Low Power VLSI Designs is written for VLSI design engineers CAD professionals and students who have had a basic knowledge of CMOS digital design and logic synthesis **Information Technology Applications in Industry II** Prasad Yarlagadda,Su Fen Yang,Keon Myung Lee,2013-09-03 Selected peer reviewed papers from the 2013 2nd International Conference on Information Technology and Management Innovation ICITMI 2013 July 23 24 2013 Zhuhai China ICCCE 2018 Amit Kumar,Stefan Mozar,2018-08-31 This book comprises selected articles from the International Communications Conference ICC 2018 held in Hyderabad India in 2018 It offers in depth information on the latest developments in voice data image and multimedia processing research and applications and includes contributions from both academia and industry Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology ,2000 Advances in Medical Physics and Healthcare Engineering Moumita Mukherjee,J.K. Mandal,Siddhartha Bhattacharyya,Christian Huck,Satarupa Biswas,2021-06-17 This book presents research advances in the theory of medical physics and its application in various sectors of biomedical engineering It gathers best selected research papers presented at International Conference on Advances in Medical Physics and Healthcare Engineering AMPHE 2020 organized by the Department of Physics in collaboration with the School of Engineering and Technology Adamas University Kolkata India The theme of the book is interdisciplinary in nature it interests students researchers and faculty members from biomedical engineering biotechnology medical physics life sciences material science and also from electrical electronics and mechanical engineering backgrounds nurturing applications in biomedical domain **Introduction to Low-Power Design**

in VLSIs Patrick Lee,2011-12-12 This book discusses one increasingly important issue in the VLSI design low power It covers the following topics a basic concepts of low power design b low power design methods and applications in industry chips and c commercial CAD tools on low power design This book discusses the concepts a set of known methods industry cases and CAD tools on the low power design It is organized in four chapters and a glossary is provided at the end of the book **Low Power Design Methodologies** Jan M. Rabaey,Massoud Pedram,2012-12-06 Low Power Design Methodologies presents the first in depth coverage of all the layers of the design hierarchy ranging from the technology circuit logic and architectural levels up to the system layer The book gives insight into the mechanisms of power dissipation in digital circuits and presents state of the art approaches to power reduction Finally it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments The individual chapters are written by the leading researchers in the area drawn from both industry and academia Extensive references are included at the end of each chapter Audience A broad introduction for anyone interested in low power design Can also be used as a text book for an advanced graduate class A starting point for any aspiring researcher [IEEE Circuits & Devices](#) ,2005

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