

VIVEK SARKAR

Partitioning and Scheduling Parallel Programs for Multiprocessors

The MIT Press

Partitioning And Scheduling Parallel Programs For Multiprocessors

Minyi Guo



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Partitioning and Scheduling Parallel Programs for Execution on Multiprocessors Vivek Sarkar, Stanford University. Computer Systems Laboratory, 1987

Partitioning and Scheduling Parallel Programs for Multiprocessors Vivek Sarkar, 1989 This book is one of the first to address the problem of forming useful parallelism from potential parallelism and to provide a general solution The book presents two approaches to automatic partitioning and scheduling so that the same parallel program can be made to execute efficiently on widely different multiprocessors The first approach is based on a macro dataflow model in which the program is partitioned into tasks at compile time and the tasks are scheduled on processors at run time The second approach is based on a compile time scheduling model where both the partitioning and scheduling are performed at compile time Both approaches have been implemented in partition programs written in the single assignment language SISAL The inputs to the partitioning and scheduling algorithms are a graphical representation of the parallel program and a list of parameters describing the target multiprocessor Execution profile information is used to derive compile time estimates of execution times and data sizes in the program Both the macro dataflow and compile time scheduling problems are expressed as optimization problems and are shown to be NP complete in the strong sense Efficient approximation algorithms for these problems are presented Finally the effectiveness of the partitioning and scheduling algorithms is studied by multiprocessor simulations of various SISAL benchmark programs for different target multiprocessor parameters Vivek Sarkar is a Member

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Parallel and Distributed Processing and Applications Minyi Guo, 2003 This book constitutes the refereed proceedings of the International Symposium on Parallel and Distributed Processing and Applications ISPA 2003 held in Aizu Japan in July 2003 The 30 revised full papers and 9 revised short papers presented together with abstracts of 4 keynotes were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on applications on Web based and intranet systems compiler and optimization techniques network routing performance evaluation of parallel systems wireless communication and mobile computing parallel topology data mining and evolutionary computing image processing and modeling network security and database and multimedia systems Compile-Time Partitioning and Scheduling of Parallel Programs Vivek Sarkar, John Hennessy, STANFORD UNIV CA COMPUTER SYSTEMS LAB., 1987 Partitioning and scheduling techniques are necessary to implement parallel languages on multiprocessors Multiprocessor performance is maximized when parallelism between tasks is optimally traded off with communication and synchronization overhead The authors present compile time partitioning and scheduling techniques to achieve this tradeoff One of the biggest challenges facing compiler writers is to efficiently implement programming languages on multiprocessors We need to find compilation techniques for general purpose parallel languages these techniques should be adaptable to a wide range of multiprocessor architectures There are three fundamental problems to be solved when compiling a program for parallel execution on a multiprocessor 1 Identifying potential parallelism 2 Partitioning the program into sequential tasks and 3 Scheduling the concurrent execution of these tasks This document addresses the latter two problems and suggest that these be solved at compile time instead of run time for applications with fairly predictable execution times Languages and Compilers for Parallel Computing Utpal Banerjee, 1994-01-28 This book contains papers selected for presentation at the Sixth Annual Workshop on Languages and Compilers for Parallel Computing The workshop was hosted by the Oregon Graduate Institute of Science and Technology All the major research efforts in parallel languages and compilers are represented in this workshop series The 36 papers in the volume are grouped under nine headings dynamic data structures parallel languages High Performance Fortran loop transformation logic and dataflow language implementations fine grain parallelism scalar analysis parallelizing compilers and analysis of parallel programs The book represents a valuable snapshot of the state of research in the field in 1993 *Proceedings of the 1993 International Conference on Parallel Processing* Alok N. Choudhary, P. Bruce Berra, 1993-08-16 This three volume work presents a compendium of current and seminal papers on parallel distributed processing offered at the 22nd International Conference on Parallel Processing held August 16 20 1993 in Chicago Illinois Topics include processor architectures mapping algorithms to parallel systems performance evaluations fault diagnosis recovery and tolerance cube networks portable software synchronization compilers hypercube computing and

image processing and graphics Computer professionals in parallel processing distributed systems and software engineering will find this book essential to their complete computer reference library **Compile-Time Partitioning and Scheduling of Parallel Programs. Extended Summary** Vivek Sarkar, John Hennessy, STANFORD UNIV CA COMPUTER SYSTEMS LAB., 1986 One of the biggest challenges facing language designers and implementors is to develop languages that will be suitable for use on multiprocessors A wide variety of multiprocessor architectures are currently being built and that variety is expected to increase Very little software is available to exploit parallelism and speed up the execution of individual programs What little work has been done has focused largely on special purpose parallelism e g vectorization or very limited classes of architectures To understand the issues involved in taking advantage of parallelism and to evaluate architectures we need to find compilation techniques for fairly general purpose languages these techniques need to be adapted to a wide variety of architectures Only then will it be possible to compare various languages alternative architectures and their interaction with different applications To compile and execute a program in a parallel fashion on a multiprocessor there are three fundamental problems to be solved 1 Identification of potential parallelism discovering parallel computations in the program 2 Partitioning the program into tasks each task represents a serially executed component that can execute in parallel with other tasks 3 Scheduling the execution of tasks the tasks must be scheduled for execution on multiple processors

Hardware/Software Co-design for Heterogeneous Multi-core Platforms Koen Bertels, 2012-02-02 HW SW Co Design for Heterogeneous Multi Core Platforms describes the results and outcome of the FP6 project which focuses on the development of an integrated tool chain targeting a heterogeneous multi core platform comprising of a general purpose processor ARM or powerPC a DSP the diopsis and an FPGA The tool chain takes existing source code and proposes transformations and mappings such that legacy code can easily be ported to a modern multi core platform Downloadable software will be provided for simulation purposes **AI 2002: Advances in Artificial Intelligence** Bob McKay, John Slaney, 2003-07-01 This book constitutes the refereed proceedings of the 15th Australian Joint Conference on Artificial Intelligence AI 2002 held in Canberra Australia in December 2002 The 62 revised full papers and 12 posters presented were carefully reviewed and selected from 117 submissions The papers are organized in topical sections on natural language and information retrieval knowledge representation and reasoning deduction learning theory agents intelligent systems Bayesian reasoning and classification evolutionary algorithms neural networks reinforcement learning constraints and scheduling neural network applications satisfiability reasoning machine learning applications fuzzy reasoning and case based reasoning

Parallel Programming and Java World Occam and Transputer User Group. Technical Meeting, 1997 **Energy Research Abstracts**, 1988 **Execution Models for Mapping Programs Onto Distributed Memory Parallel Computers** Alan Sussman, Institute for Computer Applications in Science and Engineering, 1992 Abstract This paper addresses the problem of exploiting the parallelism available in a program to efficiently employ the resources of the target machine in the context of

building a mapping compiler for a distributed memory parallel machine The paper describes using execution models to drive the process of mapping a program in the most efficient way onto a particular machine Through analysis of the execution models for several mapping techniques for one class of programs we show that the selection of the best technique for a particular program instance can make a significant difference in performance On the other hand the results of benchmarks from an implementation of a mapping compiler show that our execution models are accurate enough to select the best mapping technique for a given program

Shared Memory Multiprocessing Norihisa Suzuki,1992 Shared memory multiprocessors are becoming the dominant architecture for small scale parallel computation This book is the first to provide a coherent review of current research in shared memory multiprocessing in the United States and Japan It focuses particularly on scalable architecture that will be able to support hundreds of microprocessors as well as on efficient and economical ways of connecting these fast microprocessors The 20 contributions are divided into sections covering the experience to date with multiprocessors cache coherency software systems and examples of scalable shared memory multiprocessors

Computers and Artificial Intelligence ,1991 **Languages and Compilers for Parallel Computing** ,1994 *Вычислительные Машины И Искусственный Интеллект* ,1991 *Proceedings of the International Conference on Application Specific Array Processors* José A. Fortes,Edward A. Lee,Teresa H. Meng,1992

1996 International Conference on Parallel and Distributed Systems IEEE Computer Society. Technical Committee on Parallel Processing,1996 Proceedings of the June 1996 conference which provided a forum for scientists engineers and computer users to exchange and compare their experiences new ideas and research results Contains 169 contributions representing 24 countries over five continents discussing various aspects of distributed **Proceedings of the International Conference on Multimedia Computing and Systems** ,1999

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