

**The Technology of Parallel
Processing: Parallel
Processing Architectures and
Very Large Scale Integration
Hardware, Volume I**

Decegama, Angel L.

Parallel Processing On Very Large Scale Integration Arrays

Josef A. Nossek



Parallel Processing On Very Large Scale Integration Arrays:

Third Caltech Conference on Very Large Scale Integration R. Bryant, 2012-12-06 The papers in this book were presented at the Third Caltech Conference on Very Large Scale Integration held March 21-23 1983 in Pasadena California The conference was organized by the Computer Science Department California Institute of Technology and was partly supported by the Caltech Silicon Structures Project This conference focused on the role of systematic methodologies theoretical models and algorithms in all phases of the design verification and testing of very large scale integrated circuits The need for such disciplines has arisen as a result of the rapid progress of integrated circuit technology over the past 10 years This progress has been driven largely by the fabrication technology providing the capability to manufacture very complex electronic systems reliably and at low cost At this point the capability to manufacture very large scale integrated circuits has exceeded our capability to develop new product designs quickly reliably and at a reasonable cost As a result new designs are undertaken only if the production volume will be large enough to amortize high design costs products first appear on the market well past their announced delivery date and reference manuals must be amended to document design flaws Recent research in universities and in private industry has created an emerging science of very large scale integration *VLSI for Pattern Recognition and Image Processing* K.S. Fu, 2013-03-08 During the past two decades there has been a considerable growth in interest in problems of pattern recognition and image processing PRIP This interest has created an increasing need for methods and techniques for the design of PRIP systems PRIP involves analysis classification and interpretation of data Practical applications of PRIP include character recognition remote sensing analysis of medical signals and images fingerprint and face identification target recognition and speech understanding One difficulty in making PRIP systems practically feasible and hence more popularly used is the requirement of computer time and storage This situation is particularly serious when the patterns to be analyzed are quite complex Thus it is of the utmost importance to investigate special computer architectures and their implementations for PRIP Since the advent of VLSI technology it is possible to put thousands of components on one chip This reduces the cost of processors and increases the processing speed VLSI algorithms and their implementations have been recently developed for PRIP This book is intended to document the recent major progress in VLSI system design for PRIP applications **Scientific and Technical Aerospace Reports**, 1989

Third Caltech Conference on Very Large Scale Integration Randal E. Bryant, 1983 *Parallel Processing on VLSI Arrays* Josef A. Nossek, 2012-12-06 Guest Editor JOSEF A NOSSEK This is a special issue of the Journal of VLSI Signal Processing comprising eight contributions invited for publication on the basis of novel work presented in a special session on Parallel Processing on VLSI Arrays at the International Symposium on Circuits and Systems ISCAS held in New Orleans in May 1990 Massive parallelism to cope with high speed requirements stemming from real time applications and the restrictions in architectural and circuit design such as regularity and local connectedness brought about by the VLSI technology are the key

questions addressed in these eight papers They can be grouped into three subsections elaborating on Simulation of continuous physical systems i e numerically solving partial differential equations Neural architectures for image processing and pattern recognition Systolic architectures for implementing regular and irregular algorithms in VLSI technology The paper by A Fettweis and O Nitsche advocates a signal processing approach for the numerical integration of partial differential equations PD Es It is based on the principles of multidimensional wave digital filters MDWDFs thereby preserving the passivity of energy dissipating physical systems It is particularly suited for systems of PDEs involving time and finite propagation speed The basic ideas are explained using Maxwell s equa tions as a vehicle for the derivation of a multidimensional equivalent circuit representing the spatially infinitely extended arrangement with only very few circuit elements

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web donald robertson is the author of seven books including how to think like a roman emperor the graphic novel verissimus about the life and philosophy of marcus