

Parallel Digital Implementations of Neural Networks

Przytula, K. Wojtek

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Parallel Digital Implementations Of Neural Networks

Santosh K. Kurinec, Sumeet Walia

Parallel Digital Implementations Of Neural Networks:

Parallel Digital Implementations of Neural Networks K. Wojtek Przytula,V. K. Prasanna Kumar,1993 Explores issues related to implementing artificial neural networks on programmable massively parallel computers and special purpose digital programmable VLSI architectures The nine contributions cover mapping methodologies and implementations digital neurocomputers and architectural building blocks Annotation copyright by Book News Inc Portland OR **VLSI — Compatible Implementations for Artificial Neural Networks** Sied Mehdi Fakhraie,Kenneth C. Smith,2012-12-06 This book introduces several state of the art VLSI implementations of artificial neural networks ANNs It reviews various hardware approaches to ANN implementations analog digital and pulse coded The analog approach is emphasized as the main one taken in the later chapters of the book The area of VLSI implementation of ANNs has been progressing for the last 15 years but not at the fast pace originally predicted Several reasons have contributed to the slow progress with the main one being that VLSI implementation of ANNs is an interdisciplinary area where only a few researchers academics and graduate students are willing to venture The work of Professors Fakhraie and Smith presented in this book is a welcome addition to the state of the art and will greatly benefit researchers and students working in this area Of particular value is the use of experimental results to backup extensive simulations and in depth modeling The introduction of a synapse MOS device is novel The book applies the concept to a number of applications and guides the reader through more possible applications for future work I am confident that the book will benefit a potentially wide readership M I Elmasry University of Waterloo Waterloo Ontario Canada Preface Neural Networks NNs generally defined as parallel networks that employ a large number of simple processing elements to perform computation in a distributed fashion have attracted a lot of attention in the past fifty years As the result many new discoveries have been made

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performance improvements across most all application spaces are now severely limited by the energy dissipation involved in processing storing and moving data The exponential increase in the volume of data to be handled by our computational infrastructure is driven in large part by unstructured data from countless sources This book explores revolutionary device concepts associated circuits and architectures that will greatly extend the practical engineering limits of energy efficient computation from device to circuit to system level With chapters written by international experts in their corresponding field the text investigates new approaches to lower energy requirements in computing Features Has a comprehensive coverage of various technologies Written by international experts in their corresponding field Covers revolutionary concepts at the device circuit and system levels Pattern Recognition and Machine Intelligence Sanghamitra Bandyopadhyay,2005-12-09 This book constitutes the refereed proceedings of the First International Conference on Pattern Recognition and Machine Intelligence PReMI 2005 held in Kolkata India in December 2005 The 108 revised papers presented together with 6 keynote talks and 14 invited papers were carefully reviewed and selected from 250 submissions The papers are organized in topical sections on clustering feature selection and learning classification neural networks and applications fuzzy logic and applications optimization and representation image processing and analysis video processing and computer vision image retrieval and data mining bioinformatics application Web intelligence and genetic algorithms as well as rough sets case based reasoning and knowledge discovery Parallel Algorithms for Digital Image Processing, Computer Vision and Neural Networks Ioannis Pitas,1993-04-09 World renowned contributors present papers concerning algorithms used on the latest generation of parallel machines MIMD Details key applications running the gamut from medical imaging visualization and remote sensing to HDTV demonstrating the large computational complexity necessary to perform these tasks

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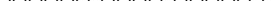
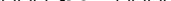
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