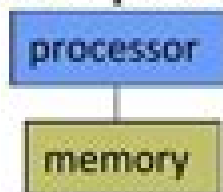


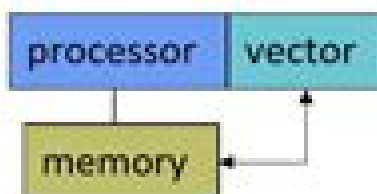
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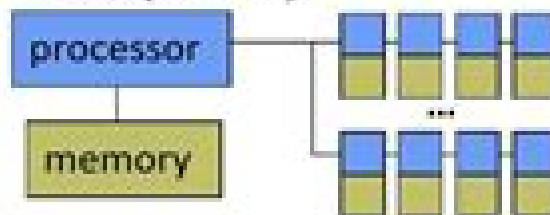
- Scalar processor



- Vector processor



- Single Instruction Multiple Data (SIMD)



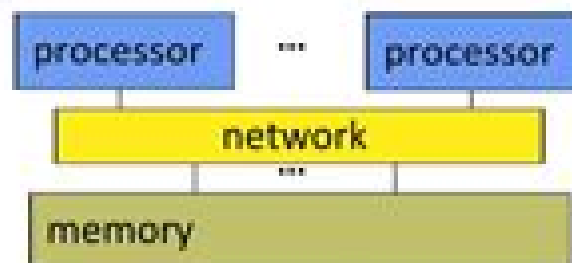
- Shared Memory

- Multiprocessor (SMP)

- Shared memory address space
    - Bus-based memory system



- Interconnection network



# Parallel Computer Architecture A Hardwaresoftware Approach

**Mark D. Hill, Gurindar S. Sohi**



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