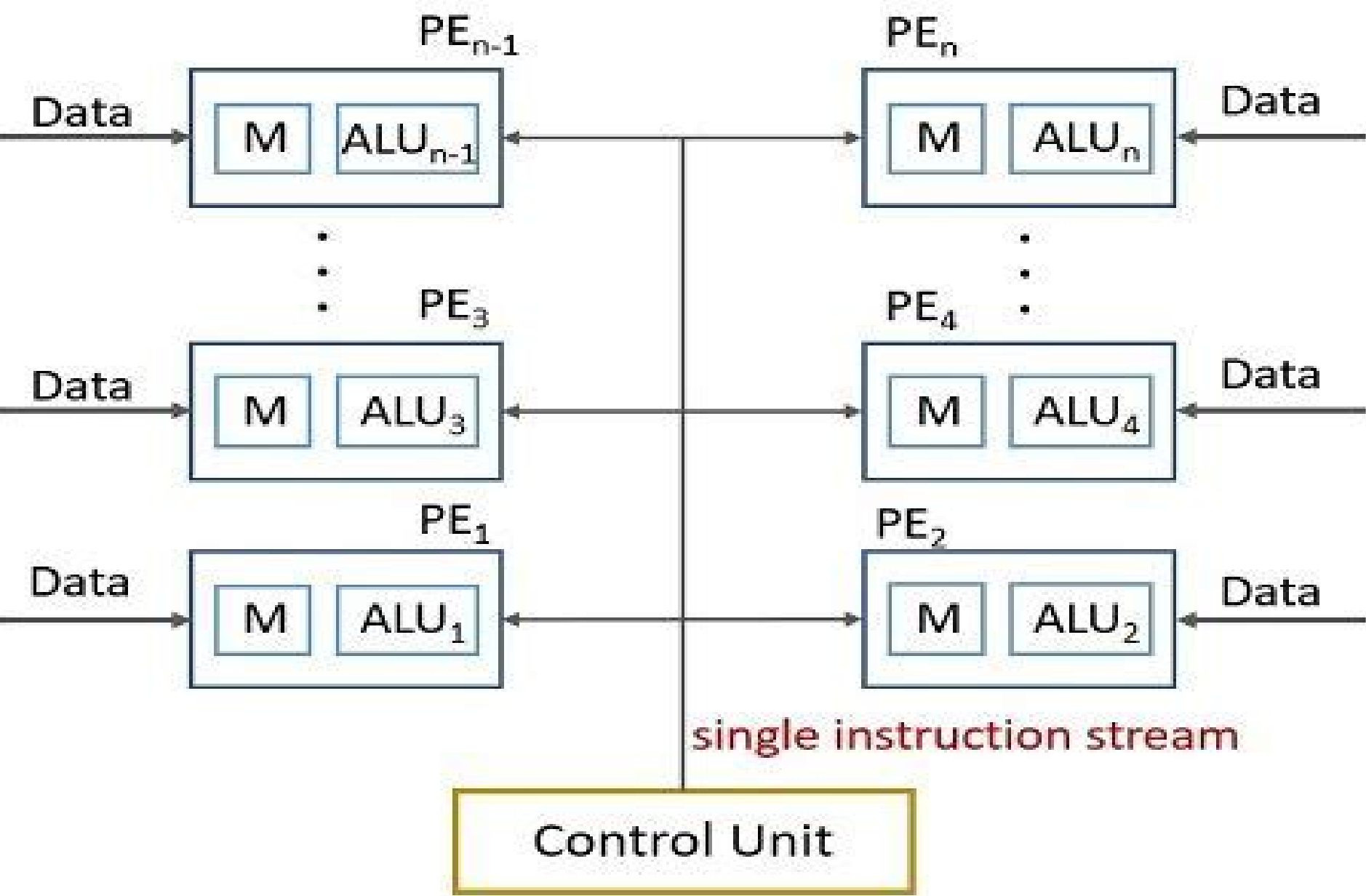




Array Processor Architecture

Multiprocessors And Array Processors

R. IBBETT



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increase of up to n times Research into parallel computations has been carried out for several years in many countries and many results of fundamental importance have been obtained Many parallel computers have been designed and their algorithmic and program ming systems built Such computers include ILLIAC IV DAP STARAN OMEN STAR 100 TEXAS INSTRUMENTS ASC CRAY 1 C mmp CM CLIP 3 PEPE This trend is supported by the fact that a many algorithms and programs are highly parallel in their structure b the new LSI and VLSI technologies have allowed processors to be combined into large parallel structures c greater and greater demands for speed and reliability of computers are made

Multiprocessors David Alejandro Padua Haiek,1979 Multiprocessors and Runtime Compilation Joel Saltz,Institute for Computer Applications in Science and Engineering,1990 Abstract Runtime time preprocessing plays a major role in many efficient algorithms in computer science as well as playing an important role in exploiting multiprocessor architectures We give examples that elucidate the importance of run time preprocessing and show how these optimizations can be integrated into compilers To support our arguments we describe transformations implemented in prototype multiprocessor compilers and present benchmarks from the iPSC2 860 the CM 2 and the Encore Multimax 320 *Multiprocessors and Array Processors: Proceedings of the Third Conference on Mulitprocessors and Array Processors, 14-16 Jan. 1987, San Diego, CA.* Walter J. Karplus (ed),1987 *Architecture of High Performance Computers: Array processors and multiprocessor systems* Roland N. Ibbett,Nigel P. Topham,1989 **VLSI Array Processors** Sun Yuan Kung,1988 **Architecture of High Performance Computers: Array processors and multiprocessor systems** Roland N. Ibbett,Nigel P. Topham,1989

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