MODELING WITH AN ANALOG HARDWARE DESCRIPTION LANGUAGE

by

H. Alan Mantooth
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the language of science

Modeling With An Analog Hardware Description Language

Thomas Griffiths

Modeling With An Analog Hardware Description Language:

   Modeling with an Analog Hardware Description Language H. Alan Mantooth,Mike F. Fiegenbaum,1994-11-30 Modeling with an Analog Hardware Description Language spells out in general terms what modeling with an analog hardware description language AHDL adds to the existing field of computer simulation using specific examples to develop this understanding The book is divided into three major sections Fundamentals of Modeling provides an overview of general modeling and simulation concepts that are used in subsequent chapters These introductory chapters cover topics such as macromodels behavioral models primitive device models modeling hierarchy top down design non electrical technologies and the Newton Raphson iterative simulation technique These topics are presented to help further the understanding of what is needed to develop models in an AHDL Model Implementation begins to convey the implementation details of the MAST AHDL The chapters in this section show how to use the governing equations of several commonly used models along with equations that are readily available from well known textbooks and papers This information is provided in both tutorial and reference fashion serving as an introduction to the basics of the MAST ADHL Each chapter builds on the information from preceding chapters in order to demonstrate progressively more complex modeling concepts This culminates with the diode and MOSFET models given in Chapter 9 which are intended to show the depth of the MAST language and which may be of interest to a more specialized segment of the modeling population Advanced Applications contains several examples of designs that use models written in the MAST ADHL Each example makes use of concepts brought up in the first two sections The main purpose of these chapters is to illustrate the importance of using an AHDL to enhance the power of computer simulations   **Analog and Mixed-Signal Hardware Description Language** A. Vachoux,Jean-Michel Bergé,Oz Levia,Jacques Rouillard,2012-12-06 Hardware description languages HDL such as VHDL and Verilog have found their way into almost every aspect of the design of digital hardware systems Since their inception they gradually proved to be an essential part of modern design methodologies and design automation tools ever exceeding their original goals of being description and simulation languages Their use for automatic synthesis formal proof and testing are good examples So far HDLs have been mainly dealing with digital systems However integrated systems designed today require more and more analog parts such as A D and D A converters phase locked loops current mirrors etc The verification of the complete system therefore asks for the use of a single language Using VHDL or Verilog to handle analog descriptions is possible as it is shown in this book but the real power is coming from true mixed signal HDLs that integrate discrete and continuous semantics into a unified framework Analog HDLs AHDL are considered here a subset of mixed signal HDLs as they intend to provide the same level of features as HDLs do but with a scope limited to analog systems possibly with limited support of discrete semantics Analog and Mixed Signal Hardware Description Languages covers several aspects related to analog and mixed signal hardware description languages including The use of a digital HDL for the description and the simulation of analog

systems The emergence of extensions of existing standard HDLs that provide true analog and mixed signal HDLs The use of analog and mixed signal HDLs for the development of behavioral models of analog electronic building blocks operational amplifier PLL and for the design of microsystems that do not only involve electronic parts The use of a front end tool that eases the description task with the help of a graphical paradigm yet generating AHDL descriptions automatically Analog and Mixed Signal Hardware Description Languages is the first book to show how to use these new hardware description languages in the design of electronic components and systems It is necessary reading for researchers and designers working in electronic design   **Analog Behavioral Modeling with the Verilog-A Language** Dan FitzPatrick,Ira Miller,2007-05-08 Analog Behavioral Modeling With The Verilog A Language provides the IC designer with an introduction to the methodologies and uses of analog behavioral modeling with the Verilog A language In doing so an overview of Verilog A language constructs as well as applications using the language are presented In addition the book is accompanied by the Verilog A Explorer IDE Integrated Development Environment a limited capability Verilog A enhanced SPICE simulator for further learning and experimentation with the Verilog A language This book assumes a basic level of understanding of the usage of SPICE based analog simulation and the Verilog HDL language although any programming language background and a little determination should suffice From the Foreword Verilog A is a new hardware design language HDL for analog circuit and systems design Since the mid eighties Verilog HDL has been used extensively in the design and verification of digital systems However there have been no analogous high level languages available for analog and mixed signal circuits and systems Verilog A provides a new dimension of design and simulation capability for analog electronic systems Previously analog simulation has been based upon the SPICE circuit simulator or some derivative of it Digital simulation is primarily performed with a hardware description language such as Verilog which is popular since it is easy to learn and use Making Verilog more worthwhile is the fact that several tools exist in the industry that complement and extend Verilog s capabilities Behavioral Modeling With the Verilog A Language provides a good introduction and starting place for students and practicing engineers with interest in understanding this new level of simulation technology This book contains numerous examples that enhance the text material and provide a helpful learning tool for the reader The text and the simulation program included can be used for individual study or in a classroom environment Dr Thomas A DeMassa Professor of Engineering Arizona State University   **System on Chip Design Languages** Anne Mignotte,Eugenio Villar,Lynn Horobin,2013-04-17 This book is the third in a series of books collecting the best papers from the three main regional conferences on electronic system design languages HDLCon in the United States APCHDL in Asia Pacific and FDL in Europe Being APCHDL bi annual this book presents a selection of papers from HDLCon Ol and FDL OI HDLCon is the premier HDL event in the United States It originated in 1999 from the merging of the International Verilog Conference and the Spring VHDL User s Forum The scope of the conference expanded from specialized languages such as VHDL and Verilog to general

purpose languages such as C and Java In 2001 it was held in February in Santa Clara CA Presentations from design engineers are technical in nature reflecting real life experiences in using HDLs EDA vendors presentations show what is available and what is planned for design tools that utilize HDLs such as simulation and synthesis tools The Forum on Design Languages FDL is the European forum to exchange experiences and learn of new trends in the application of languages and the associated design methods and tools to design complex electronic systems FDL OI was held in Lyon France around seven interrelated workshops Hardware Description Languages Analog and Mixed signal Specification C C HW SW Specification and Design Design Environments Languages Real Time specification for embedded Systems Architecture Modeling and Reuse and System Specification Design Languages **International Conference on Simulation and Hardware Description Languages (ICSHDL)** Philip A. Wilsey,David Rhodes,1994 **Modeling in Analog Design** Jean-Michel Bergé,Oz Levia,Jacques Rouillard,2012-12-06 Modeling in Analog Design highlights some of the most pressing issues in the use of modeling techniques for design of analogue circuits Using models for circuit design gives designers the power to express directly the behaviour of parts of a circuit in addition to using other pre defined components There are numerous advantages to this new category of analog behavioral language In the short term by favouring the top down design and raising the level of description abstraction this approach provides greater freedom of implementation and a higher degree of technology independence In the longer term analog synthesis and formal optimisation are targeted Modeling in Analog Design introduces the reader to two main language standards VHDL A and MHDL It goes on to provide in depth examples of the use of these languages to model analog devices The final part is devoted to the very important topic of modeling the thermal and electrothermal aspects of devices This book is essential reading for analog designers using behavioral languages and analog CAD tool development environments who have to provide the tools used by the designers Analog and Mixed-signal Hardware Description Languages Alain Vachoux,1997 Hardware description languages HDL such as VHDL and Verilog have found their way into almost every aspect of the design of digital hardware systems Since their inception they gradually proved to be an essential part of modern design methodologies and design automation tools ever exceeding their original goals of being description and simulation languages Their use for automatic synthesis formal proof and testing are good examples So far HDLs have been mainly dealing with digital systems However integrated systems designed today require more and more analog parts such as A D and D A converters phase locked loops current mirrors etc The verification of the complete system therefore asks for the use of a single language Using VHDL or Verilog to handle analog descriptions is possible as it is shown in this book but the real power is coming from true mixed signal HDLs that integrate discrete and continuous semantics into a unified framework Analog HDLs AHDL are considered here a subset of mixed signal HDLs as they intend to provide the same level of features as HDLs do but with a scope limited to analog systems possibly with limited support of discrete semantics Analog and Mixed Signal Hardware Description Languages covers several aspects related to

analog and mixed signal hardware description languages including The use of a digital HDL for the description and the simulation of analog systems The emergence of extensions of existing standard HDLs that provide true analog and mixed signal HDLs The use of analog and mixed signal HDLs for the development of behavioral models of analog electronic building blocks operational amplifier PLL and for the design of microsystems that do not only involve electronic parts The use of a front end tool that eases the description task with the help of a graphical paradigm yet generating AHDL descriptions automatically Analog and Mixed Signal Hardware Description Languages is the first book to show how to use these new hardware description languages in the design of electronic components and systems It is necessary reading for researchers and designers working in electronic design **IEEE Circuits & Devices** ,1999 **The Designer's Guide to Verilog-AMS** Ken Kundert,Olaf Zinke,2005-12-19 The Verilog Hardware Description Language Verilog HDL has long been the most popular language for describing complex digital hardware It started life as a prop etary language but was donated by Cadence Design Systems to the design community to serve as the basis of an open standard That standard was formalized in 1995 by the IEEE in standard 1364 1995 About that same time a group named Analog Verilog International formed with the intent of proposing extensions to Verilog to support analog and mixed signal simulation The first fruits of the labor of that group became available in 1996 when the language definition of Verilog A was released Verilog A was not intended to work directly with Verilog HDL Rather it was a language with Similar syntax and related semantics that was intended to model analog systems and be compatible with SPICE class circuit simulation engines The first implementation of Verilog A soon followed a version from Cadence that ran on their Spectre circuit simulator As more implementations of Verilog A became available the group defining the a log and mixed signal extensions to Verilog continued their work releasing the defi tion of Verilog AMS in 2000 Verilog AMS combines both Verilog HDL and Verilog A and adds additional mixed signal constructs providing a hardware description language suitable for analog digital and mixed signal systems Again Cadence was first to release an implementation of this new language in a product named AMS Designer that combines their Verilog and Spectre simulation engines *Hardware Description Languages* Sumit Ghosh,2000 Hardware description languages HDLs hold the key to future processor designs but until now no book has offered a clear analysis of the basic principles underlying HDLs HARDWARE DESCRIPTION LANGUAGES is the first book to unlock the often hidden science of HDLs along with their origins and basic concepts This indispensable guide explains HDLs and includes an insightful overview of the foremost HDLs of the past three decades from Computer Design Language CDL to Very High Speed Integrated Circuit VHSIC to VHSIC Hardware Description Language VHDL To improve both your knowledge and digital designs of HDL fundamentals this valuable book features these essential topics A critical review of VHDL and Verilog Accurate modeling of hardware Distributed simulation of behavior models New semantics for transport delay HARDWARE DESCRIPTION LANGUAGES is written for practicing electronic CAD engineers researchers in simulation and verification of electronic CAD graduate and

doctoral students in computer design and undergraduates specializing in electronic hardware design Professors To request an examination copy simply e mail collegeadoption ieee org Sponsored by IEEE Solid State Circuits Council Society IEEE Circuits and Systems Society   **ASIC & EDA** ,1994   **Analog VHDL** Andrzej T. Rosinski,Alain Vachoux,2012-12-06 Analog VHDL brings together in one place important contributions and up to date research results in this fast moving area Analog VHDL serves as an excellent reference providing insight into some of the most challenging research issues in the field   **VLSI Circuits and Systems** ,2003   **Proceedings of the ... International Conference on Microelectronics** ,2003   **Design, Test, and Microfabrication of MEMS and MOEMS** Bernard Courtois,1999   2001 International Conference on Modeling and Simulation of Microsystems Matthew Laudon,Bartlomiej F. Romanowicz,2001 The worlds most comprehensive and up to date collection of Multidisciplinary Micro and Nano technical papers Technical Proceedings of the 2001 International Conference on Modeling and Simulation of Microsystems Micro and Nano Fluidic Systems MEMS System Optimization MEMS Applications and Characterization Advanced Numerics Process Modeling Quantum Effects Quantum Devices Spintronics Atomistic of Silicon Processing Advanced Semiconductors Circuit Modeling Compact Modeling Papers taken from the 2001 MSM Hilton Head Island USA March 2001   **A Practical Guide to Verilog-A** Slobodan Mijalković,2022 Discover how Verilog A is particularly designed to describe behavior and connectivity of circuits and system components for analog SPICE class simulators or for continuous time SPICE based kernels in Verilog AMS simulators With continuous updates since it s release 30 years ago this practical guide provides a comprehensive foundation and understanding to the modeling language in its most recent standard formulation With the introduction of language extensions to support compact device modeling the Verilog A has become today de facto standard language in the electronics industry for coding compact models of active and passive semiconductor devices You ll gain an in depth look at how analog circuit simulators work solving system equations modeling of components from other physical domains and modeling the same physical circuits and systems at various levels of detail and at different levels of abstraction All industry standard compact models released by Si2 Compact Model Coalition CMC as well as compact models of emerging nano electronics devices released by New Era Electronic Devices and Systems NEEDS initiative are coded in Verilog A This book prepares you for the current trends in the neuromorphic computing hardware customization for artificial intelligence applications as well as circuit design for internet of things IOT will only increase the need for analog simulation modeling and make Verilog A even more important as a multi domain component oriented modeling language Let A Practical Guide to Verilog A be the initial step in learning the extended mixed signal Verilog AMS hardware description language You will Review the hardware description and modeling language Verilog A in its most recent standard formulation Code new compact models of active and passive semiconductor devices as well as new models for emerging circuit components from different physical disciplines Extend the application of SPICE like circuit simulators to non electronics field neuromorphic thermal mechanical etc systems

Apply the initial steps towards the extended mixed signal Verilog AMS hardware description language   Hybrid Vehicle and Energy Storage Technologies Society of Automotive Engineers,2003   **Electrical Communication** ,1991   *The Designer's Guide to Verilog-Ams* Ken Kundert,Olaf Zinke,2014-01-15

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