

# Materials and Processes of Electron Devices

by

**Max Knoll**

with an introduction by H. Kasper

# Materials And Processes For Electron Devices

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## **Materials And Processes For Electron Devices:**

**Materials and Processes of Electron Devices** Max Knoll, 2012-12-06 This book is intended to be of assistance to the physicist or engineer concerned with designing and building electron devices such as high vacuum transmitter or amplifier tubes gas or vapor filled rectifiers thyristors X ray or luminescent tubes glow or incandescent lamps Geiger or ionization counters vacuum photo cells photoconductive cells selenium germanium or silicon rectifiers or transistors For this purpose extensive information is required concerning the composition behavior and handling of materials as well as a thorough knowledge of high vacuum technique necessary for processing electron devices after their assembly The text covers the preparation and working of materials used in these devices the finishing methods for vacuum tubes especially degassing pumping and getter procedures and different production steps of solid state devices This book contains about 2300 references indicated in the text by the author's name and reference number At the end of each chapter the references themselves are listed alphabetically by the author's name and with the title sometimes abbreviated In accordance with the purpose of the book first publications are quoted only when they contain up to date knowledge of the subject in question Patents are treated as references The quotation of a patent gives only a hint of the technical details described there Mentioning or not mentioning a patent does not imply a statement concerning its importance or validity or warning against imitation Expired patents are named in addition to ones still valid *Materials and Processes for Electron Devices* National Research Council (U.S.). Ad Hoc Committee on Materials and Processes for Electron Devices, 1972 **Materials and Processes for Electron Devices**, 1972 **Materials and Processes for Electron Devices**, 1972 Materials and Processes of Electron Devices B. Kazan, Max Knoll, 1959-01-01 Nanoliquid Processes for Electronic Devices Tatsuya Shimoda, 2019-02-05 This book summarizes the results of the research on how to make small electronic devices with high properties by using simple liquid processes such as coating self assembling and printing especially focusing on devices composed of silicon and oxide materials It describes syntheses and analyses of solution materials formations of solid thin films from solutions newly developed patterning methods to make devices and characterization of the developed devices In the first part of the book the research on liquid silicon Si materials is described Because the use of a liquid material is a quite new idea for Si devices this book is the first one to describe liquid Si materials for electronic devices Si devices as typified by MOS FET have been produced by using solid and gas materials This volume precisely describes a series of processes from material synthesis to device fabrication for those who are interested and are will be engaged in liquid Si related work In the latter part of the book a general method of how to make good oxide films from solutions and a new imprinting method to make nanosized patterns are introduced For making oxide films with high quality the designing of the solution is crucial If a solution is designed properly a gel material called cluster gel can be formed which is able to be imprinted to form nanosized patterns The anticipated readers of this book are researchers engineers and students who are interested in solution and

printing processes for making devices More generally this book will also provide guidelines for corporate managers and executives who are responsible for making strategies for future manufacturing processes *Materials and Processes for Electron Devices. National Academy of Sciences, NAS, National Research Council, National Materials Advisory Board, NMAB. National academy of sciences. National research council, National materials advisory board, NMAB., 1972*

**Methods of Measurement for Semiconductor Materials, Process Control, and Devices** United States. National Bureau of Standards, 1973 **Materials and Process Characterization** Norman G. Einspruch, Graydon B. Larrabee, 2014-12-01 VLSI Electronics Microstructure Science Volume 6 Materials and Process Characterization addresses the problem of how to apply a broad range of sophisticated materials characterization tools to materials and processes used for development and production of very large scale integration VLSI electronics This book discusses the various characterization techniques such as Auger spectroscopy secondary ion mass spectroscopy X ray topography transmission electron microscopy and spreading resistance The systematic approach to the technologies of VLSI electronic materials and device manufacture are also considered This volume is beneficial to materials scientists chemists and engineers who are commissioned with the responsibility of developing and implementing the production of materials and devices to support the VLSI era Methods of Measurement for Semiconductor Materials, Process Control, and Devices; Quarterly Report United States. National Bureau of Standards, 1971 **Materials and Processes of Electron Devices. By M. Knoll ... Assisted by B. Kazan, Etc** Max KNOLL, Benjamin KAZAN, 1959 **Materials Processes Of Electron Devices** Knoll M., **Analytical and Diagnostic Techniques for Semiconductor Materials, Devices, and Processes** Bernd O. Kolbesen, 2003 ALTECH 2003 was Symposium J1 held at the 203rd Meeting of the Electrochemical Society in Paris France from April 27 to May 2 2003 Symposium M1 Diagnostic Techniques for Semiconductor Materials and Devices was part of the 202nd Meeting of the Electrochemical Society held in Salt Lake City Utah from October 21 to 25 2002 p iii *Materials and process of electron devices* M. Knoll, 1959 **Silicon Compatible Materials, Processes, and Technologies for Advanced Integrated Circuits and Emerging Applications 8** F. Roozeboom, P. J. Timans, K. Kakushima, H. Jagannathan, Z. Karim, E. P. Gusev, S. De Gendt, 2018-05-04 *Silicon Compatible Materials, Processes, and Technologies for Advanced Integrated Circuits and Emerging Applications 7* F. Roozeboom, H. Jagannathan, K. Kakushima, P. J. Timans, E. P. Gusev, Z. Karim, S. De Gendt, 2017 **Silicon Compatible Emerging Materials, Processes, and Technologies for Advanced CMOS and Post-CMOS Applications 9** F. Roozeboom, P. J. Timans, K. Kakushima, E. Gusev, Z. Karim, D. Misra, Y. S. Obeng, S. De Gendt, H. Jagannathan, 2019-05-17 This issue of ECS Transactions includes papers based on presentations from the symposium Silicon Compatible Emerging Materials Processes and Technologies for Advanced CMOS and Post CMOS Applications 9 originally held at the 235th ECS Meeting in Dallas Texas May 26 30 2019 Handbook of Advanced Electronic and Photonic Materials and Devices, Ten-Volume Set Hari Singh Nalwa, 2000-10-09 Vol 1 Semiconductors Vol 2

Semiconductors Devices Vol 3 High Tc Superconductors and Organic Conductors Vol 4 Ferroelectrics and Dielectrics Vol 5 Chalcogenide Glasses and Sol Gel Materials Vol 6 Nanostructured Materials Vol 7 Liquid Crystals Display and Laser Materials Vol 8 Conducting Polymers Vol 9 Nonlinear Optical Materials Volume 10 Light Emitting Diodes Lithium Batteries and Polymer Devices      An Introductory Guide to EC Competition Law and Practice Valentine Korah,1994      Electronic Materials and Processes Handbook Charles A. Harper,1994 Today the successful design and manufacture of electronic devices requires expertise in both materials science and manufacturing processes This reference provides electronics engineers and materials scientists with the information they need on the materials and processes currently used to fabricate interconnect and package electronic components and systems

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