

# Parallel Computation on Regular Arrays (Algorithms and Architectures for Advanced Scientific Computing)

Tchuate, Maurice

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# Parallel Computation On Regular Arrays

**Nalini Kanta Ratha**



## **Parallel Computation On Regular Arrays:**

**Parallel Computation on Regular Arrays** Maurice Tchunte, 1991      **Parallel Computing Technologies - Proceedings Of The International Conference** Nikolay N Mirenkov, 1991-08-24 The proceedings of this UNESCO supported conference consist of papers covering new trends and experiences in parallel computing technologies Emphasis is made on the practical aspects of parallel programming especially all aspects of the applications of parallel computing technologies hardware languages and software tools for parallel processing operating systems general architecture concepts enabling technologies performance measurements and the teaching of parallel processing technology      **Languages and Compilers for Parallel Computing** Chua-Huang Huang, 1996-01-24 This book presents the refereed proceedings of the Eighth Annual Workshop on Languages and Compilers for Parallel Computing held in Columbus Ohio in August 1995 The 38 full revised papers presented were carefully selected for inclusion in the proceedings and reflect the state of the art of research and advanced applications in parallel languages restructuring compilers and runtime systems The papers are organized in sections on fine grain parallelism interprocedural analysis program analysis Fortran 90 and HPF loop parallelization for HPF compilers tools and libraries loop level optimization automatic data distribution compiler models irregular computation object oriented and functional parallelism      **Algorithm Engineering for Integral and Dynamic Problems** Lucia Rapanotti, 2001-01-23 Algorithm engineering allows computer engineers to produce a computational machine that will execute an algorithm as efficiently and cost effectively as possible given a set of constraints such as minimal performance or the availability of technology Addressing algorithm engineering in a parallel setting regular array syntheses offer powerful computation and embody best practice but often face the criticism that they are applicable only to restricted classes of algorithms Algorithm Engineering for Integral and Dynamic Problems reviews the basic principles of regular array synthesis and shows how to extend its use into classes of algorithms traditionally viewed to be beyond its domain of application The author discusses the transformation of the initial algorithm specification into a specification with data dependencies of increased regularity in order to obtain corresponding regular arrays by direct application of the standard mapping techniques The book includes a review of the basic principles of regular array synthesis followed by applications of these techniques to well known algorithms concluding with numerous case studies to illustrate the methods Researchers and practitioners in algorithm engineering will find that this text significantly extends their understanding of the applications of regular array synthesis and regular array processors beyond the traditionally narrow field of relevance

**Piecewise Regular Arrays** Thomas P Plaks, 1999-02-22 Application specific regular array processors have been widely used in signal and image processing multimedia and communication systems for example in data compression and HDTV One of the main problems of application specific computing is how to map algorithms into hardware The major achievement of the theory of regular arrays is that an algorithm represented as a data dependence graph is embedded into a Euclidean space

where the integer points are the elementary computations and the dependencies between computations are denoted by vectors between points The process of mapping an algorithm into hardware is reduced to finding for the given Euclidean space a new coordinate system that can be associated with the physical properties of space and time so called space time The power of the synthesis method is that it provides a bridge between abstract and physical representations of algorithms thus providing a methodological basis for synthesizing computations in space and in time This book will extend the existing synthesis theory by exploiting the associativity and commutativity of computations The practical upshot being a controlled increase in the dimensionality of the Euclidean space representing an algorithm This increase delivers more degrees of freedom in the choice of the space time mapping and leads subsequently to more choice in the selection of cost effective application specific designs

**Advanced Julia Programming: Comprehensive Techniques and Best Practices** Adam Jones,2024-12-11 Unlock the full potential of Julia the high performance language designed for technical computing with Advanced Julia Programming Comprehensive Techniques and Best Practices This book serves as an essential resource for both newcomers and seasoned developers eager to deepen their understanding and enhance their skills in Julia Advanced Julia Programming Comprehensive Techniques and Best Practices provides an in depth exploration of Julia s features sophisticated programming paradigms and real world applications From mastering Julia s syntax and package ecosystem to exploring advanced topics like functional programming concurrent and parallel computing and web application development this book leaves no stone unturned Each chapter is thoughtfully designed to build on the previous one creating a coherent and thorough learning experience You ll delve into key subjects such as Julia s type system multiple dispatch performance optimization metaprogramming and language interoperability With a focus on writing efficient elegant and robust code this guide offers rich practical examples exercises and case studies to immediately apply your newfound knowledge and observe its impact in practical applications Ideal for those involved in scientific research high performance computing or anyone fascinated by the expanding capabilities of Julia Advanced Julia Programming Comprehensive Techniques and Best Practices is your gateway to mastering this dynamic language Begin your journey to becoming an expert in Julia and explore the limitless possibilities of high performance programming

*Digital Signal Processing for Multimedia Systems* Keshab K. Parhi,Takao Nishitami,2018-10-03 Addresses a wide selection of multimedia applications programmable and custom architectures for the implementations of multimedia systems and arithmetic architectures and design methodologies The book covers recent applications of digital signal processing algorithms in multimedia presents high speed and low priority binary and finite field arithmetic architectures details VHDL based implementation approaches and more

*The Cumulative Book Index* ,1992 A world list of books in the English language

**Vector and Parallel Processors in Computational Science** Iain S. Duff,John Ker Reid,1985

**Reconfigurable Technology** ,2000

[The International Conference on Application Specific Array Processors](#) Peter R. Cappello,Catherine Mongenet,Guy-René Perrin,1995 Papers invited talks and

poster sessions from the July 1995 conference address problems and solutions in the field of parallel array algorithms and architectures for special purpose systems Topics include scheduling and mapping techniques design methodologies array architectures computer arith     ICFEM 2000 Shaoying Liu, John McDermid, Michael Gerard Hinchey, 2000 Annotation

Invited speakers explore the use of mathematics in software engineering legacy code and challenges and results of offering formal verification capabilities for industry standard case tools The other 19 papers cover development structuring algebraic approaches verification formal and informal notations retrenchment the theory and practice of Z and animation There is no subject index Annotation copyrighted by Book News Inc Portland OR     **Journal of Information Science and Engineering** ,1998     *2007 International Conference on Parallel Processing* IEEE Staff, 2007     VLSI Implementations for Image Communications P. Pirsch, 1993-09-27 Reports on recent advances in VLSI architectures and their implementation for video signal processing applications with emphasis on video coding for bit rate reduction Topics covered include interactions between algorithms VLSI architectures circuit techniques and CAD for microelectronics     *ACM Transactions on Programming Languages and Systems* Association for Computing Machinery, 2000     *Computer Vision Algorithms on Reconfigurable Logic Arrays* Nalini Kanta Ratha, 1996     **Multiprocessors and Array Processors** Walter J. Karplus, 1989

**Parallel Computing Technologies** ,2005     **CERN.** ,1983

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